CHAPTER ONE

BASIC CONCEPTS AND UNITS

1.1 Introduction

The study of an electrical engineering involves the analysis of the energy transfer from one form to another or from one point to another. So before beginning the actual study of an electrical engineering, it is necessary to discuss the fundamental ideas about the basic elements of an electrical engineering like electromotive force, current, resistance etc. The electricity is related with number of other types of systems like mechanical, thermal etc. To analyze such transfer, it is necessary to revise the **S.I.** units of measurement of different quantities like work, power, energy etc. in various systems.

1.2 The Structure of Matter

The structure of matter plays an important role in the understanding of fundamentals of electricity. The matter which occupies the space may be solid, liquid or gaseous. The atom is composed of three fundamental particles: neutron, proton and the electron.

Fundamental particles of matter	Symbol	Nature of charge possessed	Mass in Kg.
Neutron	n	0	1.675x10 ⁻²⁷
proton	p+	+	1.675x10 ⁻²⁷
electron	e ⁻	-	9.107x10 ⁻³¹

1.3 Concept of Charge

Key Point: Charge is an electrical property of the atomic particles of which matter consists, measured in coulombs (C).

The following table shows the different particles and charge possessed by them.

Particle	Charge possessed in Coulomb	Nature
Neutron	0	Neutral
Proton	1.602×10 ⁻¹⁹	Positive
Electron	1.602×10 ⁻¹⁹	Negative

1.3.1 Unit of Charge

As seen from the **Table 1.2** that the charge possessed by the electron is very very small hence it is not convenient to take it as the unit of charge. The unit of the measurement of the charge is Coulomb, so one coulomb charge is defined as

1 coulomb= charge on 6.24×10¹⁸ electrons

The charge associated with one electron can then be determined from

Charge/electron =
$$e = \frac{1C}{6.24 \times 10^{18}} = 1.602 \times 10^{-19}C$$

1.4 Concept of Electromotive Force and Current

- The free electrons are responsible for the flow of electric current.
- A conductor is one which has abundant free electrons. The free electrons in such a conductor are always moving in random directions.

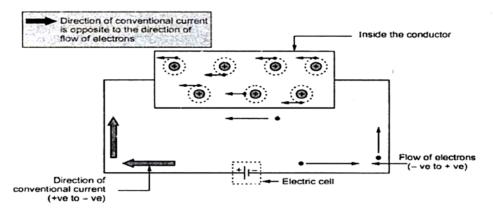


Figure 1.2 the flow of current.

The small electrical effort, externally applied to such conductor makes all such free electrons to drift along the metal in a definite particular direction. This direction depends on how the external electrical effort is applied to the conductor. Such physical phenomenon is represented in the **Fig.1.2**.

- The free electrons as are negatively charged get attracted by positive of the cell connected.
- The flow of electrons from negative to positive of the cell.
- This movement of electron is called an Electric Current. The movement of electrons is always from negative to positive while movement of current is always assumed as from positive to negative. This called direction of conventional current.

1.5 Relation between Charge and Current

The current is flow of electrons. Thus current can be measured by measuring how many electrons are passing through material per second. This can be expressed in terms of the charge carried by those electrons in the material per second.

Key Point: *Electric current is the time rate of change of charge, measured in amperes (A).*

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Mathematically we can write the relation between the charge (\mathbf{Q}) and the electric current (\mathbf{I}) as,

$$i = \frac{dq}{dt} \tag{1.1}$$

Where current is measured in amperes (A), and

1 ampere = 1 coulomb/second

The charge transferred between time t_0 and t is obtained by integrating both sides of Eq. (1.1). We obtain

$$q = \int_{t_0}^{t} idt \tag{1.2}$$

$$I = \frac{Q}{t} \quad \text{Ampere} \tag{1.3}$$

Where I = average current flowing, Q = total charge transferred

 \mathbf{t} = time required for transfer of charge.

<u>Definition of 1 Ampere:</u> A current of 1 Ampere is said to be flowing in the conductor when a charge of one coulomb is passing any given point on it in one second.

1 Ampere current = Flow of 6.24×10^{18} electrons per second

Example 1.1: Determine the time required for 4×10^{16} electrons to pass through the imaginary surface of **Fig. 1.5** if the current is 5 mA.

Solution: Determine **Q**

$$Q = 4 \times 10^{16} \frac{1C}{6.24 \times 10^{18} electron} = 0.641 \times 10^{-2} C = 6.41 mC$$
Calculate t
$$t = \frac{Q}{I} = \frac{6.41 \times 10^{-3} C}{5 \times 10^{-3} A} = 1.282 s$$

1.6 Concept of Electric Potential and Potential Difference

Key Point: potential is the energy required to move a unit charge through an element, measured in volts (V).

The electric potential at point due to a charge is one volt if one joule of work is done in moving a unit positive charge i.e. positive charge of one coulomb from Infinity to that point. Mathematically it is expressed as

Eledrical Potential = work done/charge =
$$dw/dq = W/Q$$
 (1.4)

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1 volt = 1 joule/coulomb = 1 newton.meter/coulomb

In electric circuit flow of current is always from higher electric potential to lower electric potential. So we can define potential difference as below:

Key Point: the difference between the electric potential at any two given points in a circuit is known as potential difference (p.d.) and measured in volts (V).

Thus, when two points have different potential, the electric current flows from higher potential to lower potential i.e. the electrons start flowing from lower potential to higher potential. No current can flow if the potential difference between the two points is zero.

1.7 Resistance

When the electrons begins flow in the metal. The ions get formed which are charged particles as discussed earlier. Now free electrons are moving in specific direction when connected to external source of e.m.f. So such ions always become obstruction for the flowing electrons. So there is collision between ions and free flowing electrons. This not only reduces the speed of electrons but also produced the heat. The effect of this is nothing but the reduction of flow of current. Thus the material opposes the flow of current.

Key Point: This property of an electric current circuit tending to prevent the flow of current and at the same time causes electrical energy to be converted to heat is called resistance.

The resistance is denoted by the symbol 'R' and is measured in ohm symbolically represented as Ω . We can define unit ohm as below.

Key Point: 1 Ohm: Is the resistance of a circuit, when a current of 1 Ampere generates the heat at the rate of one joules per second.

1.7.1 Factors Affecting the Resistance

- **1. Length of the material:** The Length is denoted by '*l*'.
- **2. Cross-section area:** The cross sectional area is denoted by 'a'.
- 3. The type and nature of the material:
- **4. Temperature:** The temperature of the material affects the value of the resistance.

So for a certain material at a certain temperature we can write a mathematical expression as,

$$R = \frac{\rho l}{a} \tag{1.5}$$

Where

l= length in meters,

a= cross-sectional area in square meters

 ρ = resistivity in ohms-meters, **R**= resistance in ohms

1.8 Resistivity and Conductivity

The resistivity or specific resistance of a material depends on nature of material and denoted by ρ (rho). From the eq. (1.6) of resistance it can be expressed as,

$$\rho = \frac{Ra}{l} \quad i. e. \quad \frac{\Omega - m^2}{m} = \Omega - m \tag{1.6}$$

Definition: The resistance of the material having unit length and unit cross-sectional area is known as its specific resistance or resistivity.

The Table 1.3 gives the values of resistivity of few common materials.

Material	Resistivity in Ohm-metre at 20° (×10 ⁻⁸)	Temperature coefficient at 20° (×10 ⁻⁴)
Aluminum, commercial	2.8	40.3
Brass	6-8	20
Carbon	3000-7000	-5
Lead	22	
Copper (annealed)	1.73	39.3
German silver (84% Cu; 12% Ni; 4% Zn)	20.2	2.7
Gold	2.44	36.5
Iron	9.8	65
Manganin (84% Cu; 12% Mn; 4% Ni)	44-48	0.15
Mercury	95.8	8.9
Nichrome (60% Cu; 25% Fe; 15% Cr)	108.5	1.5
Nickel	7.8	54
Platinum	9-15.5	36.7
Silver	1.64	38
Tungsten	5.5	47
Amber	5×10 ¹⁴	
Bakelite	1010	
Glass	10 ¹⁰ -10 ¹²	
Mica	1015	
Rubber	10 ¹⁶	

1.8.1 Conductance (G)

The conductance of any material is reciprocal of its resistance and ill denoted as **G**. It is the indication of ease with which current can flow through the material. It is measured in Siemens.

So
$$G = \frac{1}{R} = \frac{a}{\rho l} = \frac{1}{\rho} \left(\frac{a}{l}\right) = \sigma\left(\frac{a}{l}\right)$$
 (1.7)

1.8.2 Conductivity

The quantity $(1/\rho)$ is called conductivity denoted as σ (**sigma**). Thus the conductivity is the reciprocal of resistivity. It is measured in **Siemens/m**.

Key Point: A material having highest value of conductivity is the best conductor while with poorest value of conductivity is the best insulator.

Example 1.2: A coil consists of 2000 turns of copper wire having a cross-sectional area of 0.8 mm². The mean length per turn is 80 cm and the resistivity of copper is $0.02\mu\Omega$ -m. Find the resistance of the coil?

Solution:

Length of the coil, l = 0.8 * 2000 = 1600 m

A = 0.8 mm² = 0.8 * 10⁻⁶ m².
R =
$$\rho$$
 (l / A) = 0.02 * 10⁻⁶ * 1600 / 0.8 * 10⁻⁶ = 40 Ω.

Example 1.3: The resistance of copper wire 25 m long is found to be 50 Ω . If its diameter is 1mm, calculate the resistivity of copper

Solution:
$$l=25 \text{ m}$$
, $d=1 \text{ mm}$, $R=50\Omega$
 $a=\pi/4(d^2)=\pi/4(1^2)=0.7853 \text{ mm}^2$
 $\rho=\frac{Ra}{I}=\frac{50\times0.7853\times10^{-6}}{25}=1.57\times10^{-6} \Omega\text{-m}=1.57 \mu\Omega\text{-m}$

Example 1.4: A silver wire has resistance of 2.5 Ω . What will be the resistance of a manganin wire having a diameter, half of the silver wire and length one third? The specific resistance of manganin is 30 times that of silver.

Solution: $\mathbf{R}_{s} = \text{silver resistance} = 2.5 \ \Omega, \ \mathbf{d}_{m} = \text{manganin diameter} = \mathbf{d}_{s}/2$

 $l_{\rm m}$ = manganin length= $l_{\rm s}/3$, $\rho_{\rm m}$ = manganin specific resistance = 30 $\rho_{\rm s}$

Now $\mathbf{a_s} = \pi/4(\mathbf{d_s}^2) =$. area of cross section for silver

$$R_{s} = rac{
ho_{s}l_{s}}{a_{s}} = rac{
ho_{s}l_{s}}{rac{\pi}{4}(d_{s})^{2}} = 2.5 \Omega$$
 $R_{m} = rac{
ho_{m}l_{m}}{a_{m}} = rac{30
ho_{s} imes \left(rac{l_{s}}{3}
ight)}{rac{\pi}{4}(d_{m})^{2}} = rac{10
ho_{s}l_{s}}{rac{\pi}{4}\left(rac{d_{s}}{2}
ight)^{2}}$
 $= 40 rac{
ho_{s}l_{s}}{rac{\pi}{4}(d_{s})^{2}} = 40R_{s} = 100 \Omega$ Resistance of manganin

1.9 Effect of Temperature on Resistance

The resistance of the material affected as temperature of a material change. As example, Atomic structure theory says that under normal temperature when the metal is subjected to potential difference, ions i.e. unmovable charged particles get formed inside the metal. The electrons which are moving randomly get aligned in a particular direction as shown in the **fig. 1.3**. If temperature

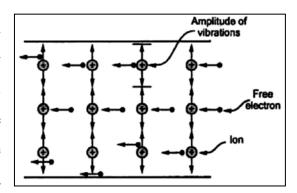


Figure 1.3 Vibrating ions in conductor.

increases, the ions gain energy and start oscillating about their mean position and cause collision and obstruction to the flowing electrons. Due to collision and obstruction due to higher amplitude of oscillations of ions, the resistance of material increases as temperature increases. But this is not true for all materials. In some cases the resistance decreases as temperature increase.

1.9.3 Effect of Temperature on Alloys

The resistance of alloys increase as the temperature increase but rate of increase is not significant. In fact, some of alloys show almost no change in resistance for considerable change in the temperature like Manganin (alloy of copper, manganese and nickel), Eureka (alloy of copper and nickel) etc. Due to this property alloys are used to manufacture the resistance boxes. **Fig.1.5** shows the effect of temperature on metals, insulating materials and alloys.

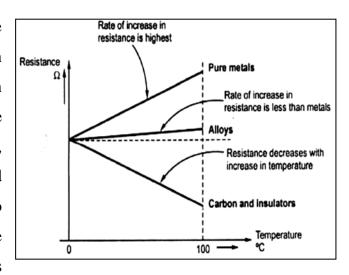


Figure 1.5 effect of temperature on resistance.

1.9.4 Effect of Temperature on Semiconductors

The materials having conductivity between that of metals and insulators are called semiconductors such as silicon, germanium etc. At absolute zero temperature, the semiconductors behave as perfect insulators.

For semiconductor materials, an increase in temperature will result in a decrease in the resistance

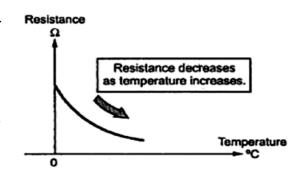


Figure 1.6 Effect of temperature on semiconductor.

level. Consequently, semiconductors have negative temperature coefficients.

The thermistor and photoconductive cell are excellent examples of semiconductor devices with negative temperature coefficients.

1.10 Resistance Temperature Coefficient (R.T.C.)

From the discussion up till now we can conclude that the change in resistance is,

- 1) Directly proportional to the initial resistance.
- 2) Directly proportional to the change in temperature.
- 3) Depends on the nature of the material whether it is a conductor, alloy or insulator.

Let us consider a conductor, the resistance of which increases with temperature linearly.

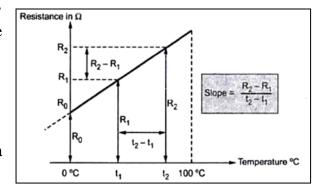


Figure 1.7 resistance vs. temperature.

Let $\mathbf{R_0}$ = Initial resistance at 0 C°, $\mathbf{R_1}$ = Resistance at $\mathbf{t_1}$ C°, $\mathbf{R_2}$ =Resistance at $\mathbf{t_2}$ C°

As shown in the **Fig. 1.7**. $R_2 > R_1 > R_0$

Definition of R.T.C.: The resistance temperature coefficient at \mathbf{t} \mathbf{C}° is the ratio of change in resistance per degree Celsius to the resistance at \mathbf{t} \mathbf{C}° . the unit of \mathbf{R} . \mathbf{T} . \mathbf{C} . is $1/\mathbf{C}^{\circ}$.

From the **Fig. 1.7**, change in resistance = \mathbf{R}_2 - \mathbf{R}_1 , change in temperature = \mathbf{t}_2 - \mathbf{t}_1

change in resistance per
$$C^{\circ} = \frac{\Delta R}{\Delta t} = \frac{R_2 - R_1}{t_2 - t_1}$$
 = the slope of graph

Hence according to the definition of **R.T.C.** we can write α_1 at t_1 C° as,

$$lpha_1 = rac{change\ in\ resistance\ per\ C^o}{resistance\ at\ t_1\ C^o} = rac{{R_2 - R_1}/{t_2 - t_1}}{R_1}$$

1.10.1 Use of R.T.C. In Calculating Resistance at t C^o

Let $\alpha_0 = \mathbf{R.T.C.}$ at $0 \, \mathrm{C}^{\circ}$, $\mathbf{R_0} = \mathrm{Resistance}$ at $0 \, \mathrm{C}^{\circ}$, $\mathbf{R_t} = \mathrm{Resistance}$ at $\mathbf{t} \, \mathrm{C}^{\circ}$

Then
$$\alpha_0 = \frac{\binom{R_t - R_0}{t_{t-0}}}{R_0} = \frac{R_t - R_0}{t_{t-0}} \implies \mathbf{R}_t = \mathbf{R}_0 (1 + \alpha_0 t)$$
 (1.8)

In general, above result can be expressed as

$$R_{final} = R_{initial} \left[1 + \alpha_{initial} \Delta t \right]$$
 (1.9)

1.10.2 Effect of Temperature on R.T.C.

From the above discussion, it is clear that the value of **R.T.C.** also changes with the temperature. As the temperature increases, its value decreases. For any metal its value is maximize at $0 \, \text{C}^{\circ}$.

If starting temperature is $\mathbf{t_1} = 0$ C° and a at \mathbf{t} C° i.e. α_t is required then we can write,

$$\alpha_t = \frac{\alpha_0}{1 + \alpha_0(t - 0)} = \frac{\alpha_0}{1 + \alpha_0 t}$$
 (1.10)

1.10.3 Effect of Temperature on Resistivity

Similar to the resistance, the specific resistance or resistivity is a function of temperature. So similar to resistance temperature coefficient we can define temperature coefficient of resistivity as fractional change in resistivity per degree centigrade change in temperature from the given reference temperature.

if
$$\rho_1$$
= resistivity at $t_1 C^0$, ρ_2 = resistivity at $t_2 C^0$

Then temperature coefficient of resistivity α at t_1 C° can be defined as,

$$\alpha_{t1} = \frac{(\rho_2 - \rho_1)/(t_2 - t_1)}{\rho_1} \tag{1.11}$$

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Similarly we can write the expression for resistivity at time t C° as,

$$\rho_{t} = \rho_{0} (1 + \alpha_{0}t)$$

$$\rho_{t2} = \rho_{t1} [1 + \alpha_{t1} (t_{2}-t_{1})]$$
(1.12)

Example 1.5: A certain winding made up of copper has a resistance of 100 Ω at room temperature. If resistance temperature coefficient of copper at 0 C° is 0.00428/ C° , calculate the winding resistance if temperature is increased to 50 C° . Assume room temperature as 25 C° .

Solution:
$$\mathbf{t}_1$$
=25C°, \mathbf{R}_1 =100 Ω, \mathbf{t}_2 = 50 C°, $\mathbf{\alpha}_0$ = 0.00428/C°
Now
$$\mathbf{\alpha}_t = \frac{\mathbf{\alpha}_0}{\mathbf{1} + \mathbf{\alpha}_0 \mathbf{t}}$$

$$\mathbf{\alpha}_1 = \frac{\mathbf{\alpha}_0}{\mathbf{1} + \mathbf{\alpha}_0 \mathbf{t}_1} = \frac{0.00428}{\mathbf{1} + 0.00428 \times 25} = 0.003866/C°$$
Use
$$\mathbf{R}_2 = \mathbf{R}_I \left[\mathbf{1} + \mathbf{\alpha}_1 \left(\mathbf{t}_2 - \mathbf{t}_1 \right) \right] = 100[1 + 0.003866(50 - 25)]$$

$$= 109.6657 \ \Omega$$
 resistance at 50 C°

Example 1.6: A specimen of copper has a resistivity (ρ) and a temperature coefficient of 1.6×10^{-6} ohm-cm at 0 C° and 1/254.5 at 20 C° respectively. Find both of them at 60 C°.

Solution:
$$\rho_0 = 1.6 \times 10^{-6} \text{ ohm-cm} = 1.6 \times 10^{-8} \text{ ohm-m}, \qquad \alpha_1 = \frac{1}{254.5} / c^o \quad \text{at } 20 \text{ C}^o$$
Now
$$\alpha_t = \frac{\alpha_0}{1 + \alpha_0 t} \qquad \alpha_1 = \frac{\alpha_0}{1 + \alpha_0 \times 20}$$

$$\frac{1}{254.5} = \frac{\alpha_0}{1 + 20\alpha_0} \qquad \Rightarrow \qquad 1 + 20\alpha_0 = 254.5 \alpha_0$$

$$\alpha_0 = \frac{1}{234.5} / c^o \quad \text{at } 0 \text{ C}^o$$

$$\alpha_{60} = \frac{\alpha_0}{1 + \alpha_0 \times 60} = \frac{1/234.5}{1 + 60/234.5} = \frac{1}{294.5} / c^o$$

$$\rho_t = \rho_0 (1 + \alpha_0 t)$$

$$\rho_{60} = 1.6 \times 10^{-8} \left(1 + \frac{1}{234.5} \times 60\right) = 2 \times 10^{-8} \Omega - m$$

1.11 Fundamental Quantities and Units

Scientists and engineers know that the terms they use, the quantities they measure must all be defined precisely. Such precise and standard measurements can be specified only if there is common system of indication of such measurements. This common system of unit is called 'SI' system i.e. International System of Units. The SI system is divided into six base units and two supplementary units. The six fundamental or base units are length, mass, time, electric current, temperature, amount of substance and luminous intensity, see table 1.4. The two

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supplementary units are plane angle and solid angle. All other units are derived which are obtained from the above two classes of units. The derived units are classified into three main groups.

1. Mechanical units, 2. Electrical units, 3. Heat units

TABLE 1.4 the six basic SI units.

Quantity	Basic unit	Symbol
Length	meter	m
Mass	kilogram	kg
Time	second	S
Electric current	ampere	A
Thermodynamic temperature	kelvin	K
Luminous intensity	candela	cd

1.11.1 Multiples and sub-multiples

One great advantage of the SI unit is that it uses prefixes based on the power of 10 to relate larger and smaller units to the basic unit. **Table 1.5** shows the **SI** prefixes and their symbols. For example, the following are expressions of the same distance in meters (m): 600,000,000 mm = 600,000 m = 600 km.

TABLE 1.5 the SI prefixes.

Multiplier	Prefix	Symbol
10 ¹⁸	exa	E
10^{15}	peta	P
10^{12}	tera	T
10 ⁹	giga	G
10^{6}	mega	M
10^{3}	kilo	k
10^{2}	hecto	h
10	deka	da
10^{-1}	deci	d
$\frac{10^{-1}}{10^{-2}}$	centi	С
10^{-3}	milli	m
10^{-6}	micro	μ
10 ⁻⁹	nano	n
10^{-12}	pico	p
10^{-15}	femto	f
10^{-18}	atto	a

✓ For American automobiles, engine power is rated in a unit called "horsepower," then

1 Horsepower = 745.7 Watt

1.11.3 Electrical Units

The various electrical units are,

1. Electrical work: In an electric circuit, movement of electrons i.e. transfers of charge is an electric current. The electrical work is done when there is a transfer of charge. The unit of such work is Joule.

So if V is potential difference in volts and Q is charge in coulombs then we can write,

Electrical work =
$$\mathbf{W} = \mathbf{V} \times \mathbf{Q} + \mathbf{J}$$
 But $\mathbf{I} = \mathbf{Q/t}$, $\mathbf{W} = \mathbf{V.I.t} + \mathbf{J}$ where $\mathbf{t} = \mathbf{time in second}$ (1.21)

2. Electrical power: The rate at which electrical work is done in an electric circuit is called an electrical power.

Electrical power =
$$P = \text{electrical work / time} = W / t = V.I.t / t$$

 $P=V.I$ J/sec i.e. watts (1.22)

Thus power consumed in an electric circuit is 1 wall if the potential difference of 1 volt applied across the circuit causes 1 ampere current to flow through it.

3. Electrical energy: An electrical energy is the total amount of electrical work done in an electric circuit.

Electrical energy =
$$\mathbf{E} = \mathbf{Power} \times \mathbf{Time} = \mathbf{V.I.t}$$
 joules (1.23)

The unit of energy is joule or watt-sec.

As watt-sec unit is very small, the electrical energy is measured in bigger units as watt-hour (**Wh**) and kilo watt-hour (**kWh**). When a power of 1 kW is utilized for 1 hour, the energy consumed is said to be **1 kWh**. This unit is called a **Unit**.

1.11.5 Efficiency

The efficiency can be defined the ratio of energy output to energy input. It can be also expressed as ratio of power output to power input. Its value is always less than 1. Higher its value, more efficient is the system of equipment. Generally it is expressed in percentage, its symbol η .

$$\% \eta = \text{Energy output/ Energy input} \times 100$$

$$= \text{Power output/ power input} \times 100$$
(1.27)

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Example 1.7: An electric pump lifts 60 m3 of water per hour to a height of 25 m. The pump efficiency is 82 % and the motor efficiency is 77 %. The pump is used for 3 hours daily. Find the energy consumed per week, if the mass of 1 - m3 of water is 1000 kg.

Solution: 1 m³ = 1000 kg hence m = 60 m³ = 60000 kg
h = 25 m,
$$\eta$$
m = 77 %, η p = 82 %, time= 1 hour= 3600 sec

Energy output =
$$mgh = 60000 \times 9.81 \times 25 = 14.715 \times 10^6 \text{ J}$$

Pout =
$$\frac{\text{energy}}{\text{time}} = \frac{14.715 \times 10^6}{3600} = 4087.5 \text{ W}$$

Pin
$$=\frac{P_{\text{out}}}{\eta_m \eta_p} = \frac{4087.5}{0.82 \times 0.77} = 6473.7092 \text{ w}$$

Per day 3 hours running hence,

Daily consumption= $6473.7092 \times 3 = 19.421 \text{ kWh}$

Weekly power consumption $= 7 \times 19.421 = 135.947 \text{ kWh}$

Weekly energy consumption = $135.947 \times 10^3 \times 3600 = 489.4124 \times 10^6 \text{ J}$

CHAPTER TWO

Basic Laws

2.1 Introduction

Chapter 1 introduced basic concepts in an electric circuit. To actually determine the values of these variables in a given circuit requires that we understand some fundamental laws that govern electric circuits. These laws, known as **Ohm's law** and **Kirchhoff's laws**, form the foundation upon which electric circuit analysis is built. In addition to these laws, we shall discuss some techniques commonly applied in circuit design and analysis.

2.2 Network Terminology

In this section, we shall define some of the basic terms which are commonly associated with a network.

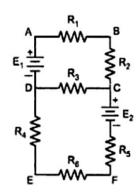
- 1. Network: Any arrangement of the various, electrical energy source along with the different circuit elements is called an electrical network. Such a network is shown in the Fig. 2.1.
- **2. Network Element:** Any individual circuit element with two terminals which can be connected to other circuit element is called a network element. Network elements can be either active elements or passive elements.
- 3. Branch: A part of the network which connects the various points of the network with one another is called a branch. In the **Fig. 2.1**, **AB**, **BC**, **CD**, **DA**, **DE**, **CF** and **EF** are the various branches. The branch may consist of more than one element.
- **4. Junction Point:** A point where three or more branches meet is called a junction point. Points D and C are the junction points in the network shown in the **Fig. 2.1**.
- 5. Node: A point at which two or more elements are joined together is called node. The junction points are also the nodes of the network. In the network shown in the Fig. 2.1, A, B, C, D, E and F are the nodes of the network.
- <u>6. Mesh (or Loop)</u>: Mesh (or Loops) is a set of branches forming a closed path in a network in such way that if one branch is removed then remaining branches do not form a closed path. In the **Fig. 2.1** paths **A-B-C-D-A**, **A-B-C-F-E-D-A**, **D-C-F-E-D** etc are the loops of the network.

In this chapter, the analysis of **d.c.** circuits consisting of pure resistors and **d.c.** sources is included.

2.3 Classification of Electric Networks

The behavior of the entire network depends on the behavior and characteristics of its elements. Based on such characteristics electrical network can be classified as below,

<u>i) Linear Network:</u> A circuit or network whose parameter i.e. elements are always constant irrespective of the change in time, voltage, temperature etc. is known as **linear network**.



<u>ii) Nonlinear Network:</u> A circuit whose parameters change their values with change in time, temperature, voltage etc. is known as **nonlinear network**.

Figure 2.1 an electrical network.

<u>iii) Active Network:</u> A circuit whose contain at least one source of energy is called active. An energy source may be a voltage or current source.

iv) Passive Network: A circuit which contains no energy source is called passive circuit. This is shown in the **Fig 2.2**.

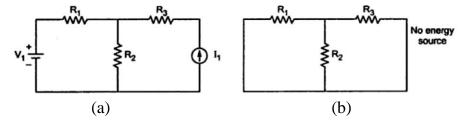


Figure 2.2 (a) active network, (b) passive network

2.4 OHM'S LAW

Ohm's law.

As shows in chapter one, the materials in general have a characteristic behavior of resisting the flow of electric charge. The resistance R of any material with a uniform cross-sectional area A depends on A and its length l.

The circuit element used to model the current-resisting behavior of a material is the resistor. For the purpose of constructing circuits, resistors are usually made from metallic alloys and carbon compounds. The circuit symbol for the resistor is shown in **Fig. 2.3**, where **R** stands for the resistance of the resistor. The resistor is the simplest passive element. Georg Simon Ohm (1787–1854), a German physicist, is credited with finding the relationship

between current and voltage for a resistor. This relationship is known as

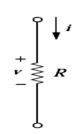


Figure 2.3 Circuit symbol for resistance.

Key Point: Ohm's law states that the voltage \mathbf{v} across a resistor is directly proportional to the current \mathbf{i} flowing through the resistor.

Ohm defined the constant of proportionality for a resistor to be the resistance; **R**. (The resistance is material property which can change if the internal or external conditions of the element are altered, e.g., if there are changes in the temperature.) Thus,

$$\mathbf{v} = i\mathbf{R} \tag{2.1}$$

The resistance R of an element denotes its ability to resist the flow of electric current; it is measured in ohms (Ω) .

Then
$$\mathbf{R} = \mathbf{v/i}$$
 (2.2) so that $\mathbf{I} \Omega = \mathbf{I} \mathbf{V/A}$

It should be pointed out that not all resistors obey Ohm's law. A resistor that obeys Ohm's law is known as a linear resistor. It has a constant resistance and thus its current-voltage characteristic is as illustrated in **Fig. 2.4(a)**. A nonlinear resistor does not obey Ohm's law. Its resistance varies with current and its i-v characteristic is typically shown in **Fig. 2.4 (b)**. Examples of devices with nonlinear resistance are the light bulb and the diode. A useful quantity in circuit analysis is the reciprocal of resistance **R**, known as conductance and denoted by **G**:

$$G = 1/R = i/v$$
 (2.3)

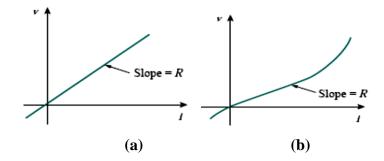


Figure 2.4 The i-v characteristic of: (a) a linear resistor, (b) a nonlinear resistor.

The conductance is a measure of how well an element will conduct electric current. The unit of conductance is the mho (ohm spelled backward) or reciprocal ohm, with symbol \mho , the inverted omega. Although engineers often use the mhos, in this lectures we prefer to use the **Siemens** (**S**), the **SI** unit of conductance:

$$1 S = 1 T = 1 A/V$$

Thus,

Conductance is the ability of an element to conduct electric current; it is measured in mhos (\mathfrak{O}) or Siemens (S).

From Eq. (2.3), we may write

$$\mathbf{i} = \mathbf{G}\mathbf{v} \tag{2.4}$$

The power dissipated by a resistor can be expressed in terms of \mathbf{R} . Using **Eqs.** (1.23) and (2.1),

$$\mathbf{p} = \mathbf{v}\mathbf{i} = \mathbf{i}^2 \mathbf{R} = \mathbf{v}^2 / \mathbf{R} \tag{2.5}$$

The power dissipated by a resistor may also be expressed in terms of G as

$$\mathbf{p} = \mathbf{v}\mathbf{i} = \mathbf{v}^2 \mathbf{G} = \mathbf{i}^2 / \mathbf{G}$$
 (2.6)

We should note two things from **Eqs.** (2.5) and (2.6):

- 1. The power dissipated in a resistor is a nonlinear function of either current or voltage.
- 2. Since **R** and **G** are positive quantities, the power dissipated in a resistor is always positive. Thus, a resistor always absorbs power from the circuit.

2.4.1 Limitations of Ohm's Law

The Limitations of the Ohm's law are,

- 1) It is not applicable to the nonlinear devices such as diode, zener diode, voltage regulators.
- 2) It does not hold good for non-metallic conductors such as silicon carbide. The law for such conductors is given by, $V = kI^m$ where **k**, **m** are constants.

EXAMPLE 2.1: An electric iron draws 2 A at 120 V. Find its resistance.

Solution:

From Ohm's law,
$$R = v/i = 120/2 = 60 \Omega$$

EXAMPLE 2.2: In the circuit shown below, calculate the current i, the conductance G, and the power **P**.

Solution:

The voltage across the resistor is the same as the source voltage (30 V) because the resistor and the voltage source are connected to the same pair of terminals. Hence, the current is

30 V
$$5 k\Omega$$
 $v = 0$

$$i = v / R = 30 \times 5 \times 10^3 = 6 \text{ mA}$$

The conductance is
$$G = 1/R = 1/5 \times 10^3 = 0.2 \text{ mS}$$

We can calculate the power in various ways using either Eqs. (1.29), (2.5), or (2.6).

$$p = vi = 30 \times (6 \times 10^{-3}) = 180 \text{ mW}$$

PRACTICE PROBLEM 2.1: For the circuit shown below, calculate the voltage v, the conductance G, and the power p.

Answer: 20 V, 100 μS, 40 mW.

2 mA 10 k Ω $\geqslant v$

2.5 SERIES RESISTORS

A series circuit is one in which several resistances are connected one after the other. There is only one path for the flow of current. Consider the resistances shown in the **Fig. 2.5**. The resistance \mathbf{R}_1 , \mathbf{R}_2 and \mathbf{R}_3 , said to be in series.

Req= Equivalent resistance of the circuit.

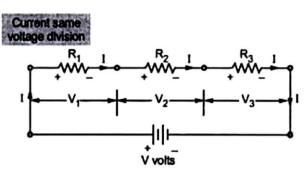


Fig. 2.5 series circuit

$$\mathbf{Req} = \mathbf{R}_1 + \mathbf{R}_2 + \mathbf{R}_3$$

i.e. total or equivalent resistance of the series circuit is arithmetic sum of the resistances connected in series.

For N resistances in series,
$$\mathbf{R} = \mathbf{R}_1 + \mathbf{R}_2 + \mathbf{R}_3 + ... + \mathbf{R}_N$$
 (2.7)

If
$$\mathbf{R}_1 = \mathbf{R}_2 = \cdots = \mathbf{R}_N = \mathbf{R}$$
, then

$$\mathbf{R}_{eq} = \mathbf{N} \times \mathbf{R} \tag{2.8}$$

2.5.1 Characteristics of Series Circuits

- 1) The same current flows through each resistance.
- 2) The supply voltage V is the sum of the individual voltage drops across the resistances.

$$V = V_1 + V_2 + V_3 + ... + V_N$$
 (2.9)

- 3) The equivalent resistance is equal to the sum of the individual resistances.
- 4) The equivalent resistance is the largest of all the individual resistances.

i.e.
$$R > R_1, R > R_2, ... R > R_N$$

2.6 PARALLEL RESISTORS

The parallel circuit is one in which several resistances are connected across one another in such a way that one terminal of each is connected to form a junction point while the remaining ends are also joined to form another junction point. Consider a parallel circuit shown in the **Fig. 2.6**.

R_{eq}= Total or equivalent resistance of the circuit,

$$\frac{1}{R_{eq}} = \frac{1}{R_1} + \frac{1}{R_2} + \frac{1}{R_3}$$

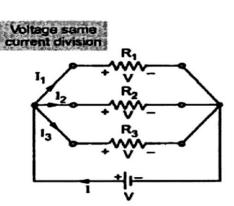


Fig. 2.6 A parallel circuit.

In general if 'N' resistances are in parallel,

$$\frac{1}{R} = \frac{1}{R_1} + \frac{1}{R_2} + \frac{1}{R_3} + \dots + \frac{1}{R_N}$$
 (2.10)

Note that **Req** is always smaller than the resistance of the smallest resistor in the parallel combination. If $\mathbf{R}_1 = \mathbf{R}_2 = \cdots = \mathbf{R}_N = \mathbf{R}$, then

$$\mathbf{R}_{eq} = \mathbf{R/N} \tag{2.11}$$

Conductance (G):

It is known that, $1/\mathbf{R} = \mathbf{G}$ (conductance) hence,

$$G = G_1 + G_2 + G_3 + ... + G_N$$
 (2.12)

Important result:

Now If N = 2, two resistance are in parallel then,.

$$\frac{1}{R} = \frac{1}{R_1} + \frac{1}{R_2} \text{ or } R = \frac{R_1 R_2}{R_1 + R_2}$$
 (2.13)

2.6.1 Characteristics of Parallel Circuits

1) The same potential difference gets across all the resistances in parallel.

- 2) The total current gets divided into the number of paths equal to the number of resistances in parallel. The total current is always sum of the individual currents.
- 3) The reciprocal of the equivalent resistance of a parallel circuit is equal to the sum of the reciprocal of the individual resistances.
- 4) The equivalent resistance is the smallest of all the resistances $\mathbf{R} < \mathbf{R}_1$, $\mathbf{R} < \mathbf{R}_2$, $\mathbf{R} < \mathbf{R}_N$.
- 5) The equivalent conductance is the arithmetic addition of the individual conductances.

In general, it is often convenient and possible to combine resistors in series and parallel and reduce a resistive network to a single equivalent resistance **Req**.

Example 2.3: Find **Req** for the circuit shown in **Fig. 1**.

To get \mathbf{R}_{eq} , we combine resistors in series and in parallel. The 6- Ω and 3- Ω resistors are in parallel, so their equivalent resistance is

$$6 \Omega \parallel 3 \Omega = 6 \times 3/(6+3) = 2 \Omega$$

(The symbol \parallel is used to indicate a parallel combination.) Also, the 1- Ω and 5- Ω resistors are in series; hence their equivalent resistance is

$$1 \Omega + 5 \Omega = 6 \Omega$$

Thus the circuit in **Fig. 1** is reduced to that in **Fig. 2(a)**. **In Fig. 2(a)**, we notice that the two 2- Ω resistors are in series, so the equivalent resistance is

$$2\Omega + 2\Omega = 4\Omega$$

This 4- Ω resistor is now in parallel with the 6- Ω resistor in Fig. 2 (a); their equivalent resistance is

$$4 \Omega || 6 \Omega = 4 \times 6/(4+6) = 2.4 \Omega$$

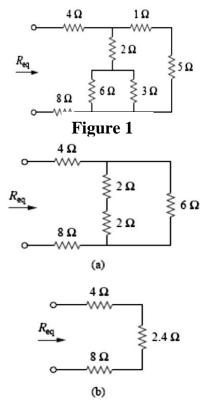


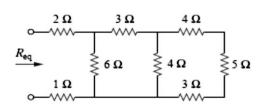
Figure 2

The circuit in Fig. 2 (a) is now replaced with that in Fig. 2 (b). In Fig. 2 (b), the three resistors are in series. Hence, the equivalent resistance for the circuit is

$$R_{eq} = 4~\Omega + 2.4~\Omega + 8~\Omega = 14.4~\Omega$$

PRACTICE PROBLEM 2.2: By combining the resistors in Figure below, find Req.

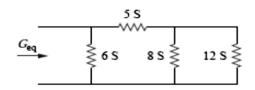
Answer: 6 Ω .



PRACTICE PROBLEM 2.3: Find the conductance G_{eq} for

the circuit in Figure below.

Answer: 10 S.

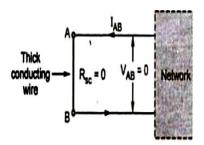


2.7 Short and Open Circuits

In the network simplification, short circuit or open circuit existing in the network plays an important role. Since the value of \mathbf{R} can range from zero to infinity, it is important that we consider the two extreme possible values of \mathbf{R} .

2.7.1 Short Circuit

When any two points in a network are joined directly to each other with a thick metalic conducting wire the two points are said to be short circuited. The resistance of such short circuit is zero.



The part of the network, which is short circuited, is shown in the **Fig. 2.7**. The points A and B are short circuited. The resistance of the branch AB is Rsc=0. The Current I_{AB} is flowing through the short circuited path. According to Ohm's law,

$$V_{AB} = R_{SC} \times I_{AB} = 0 \times I_{AB} = 0 V$$

Figure 2.7 Short circuit $(R_{sc} = 0)$

Key Point: The voltage across short circuit is always zero though current flows through the short circuited path.

2.7.2 Open Circuit

When there is no connection between the two points of a network, having some voltage across the two points then the two points are said to be open circuited.

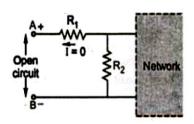


Figure 2.8 Open circuit $(R_{OC} = \infty)$.

As there is no direct connection in an open circuit, the resistance of the open circuit is ∞ . The part of the network which is open circuited is shown in the **Fig. 2.8**. The points A and B are said to be open circuited. The resistance of the branch AB is $\mathbf{R}_{\mathbf{OC}} = \infty \Omega$.

According to Ohm's law,

$$I_{OC} = V_{AB}/R_{OC} = V_{AB}/\infty = 0 A$$

Key Point: The current through open circuit is always zero though there exist voltage across open circuited terminals.

2.8 The Voltage-divider and Current-divider Circuits

2.8.1 The voltage-divider circuit

Voltage-divider circuit, shown in **Fig.2.9**. We analyze this circuit by directly applying Ohm's law and Kirchhoff's laws. To aid the analysis we introduce the current i as shown in **Fig.2.9** (b). From Kirchhoff's current law \mathbf{R}_1 and \mathbf{R}_2 , carry the same current. Applying **Kirchhoff's voltage law** around the closed loop yields

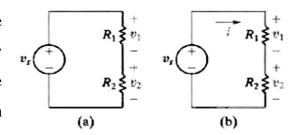


Figure 2.9 (a) A voltage-divider circuit and (b) The voltage-divider circuit with current i indicated

$$\mathbf{v}_{s} = \mathbf{i} \; \mathbf{R}_{1} + \mathbf{i} \; \mathbf{R}_{2} \; ,$$

Now we can use Ohm's law to calculate v_1 and, v_2 :

$$v_1 = \frac{R_1 v_s}{R_1 + R_2}, \quad v_2 = \frac{R_2 v_s}{R_1 + R_2}$$
 (2.14)

In general, if a voltage divider has N resistors $(\mathbf{R}_1, \mathbf{R}_2, \dots, \mathbf{R}_N)$ in series with the source voltage v_s , the Nth resistor (\mathbf{R}_N) will have a voltage drop of

$$v_N = \frac{R_N v_s}{R_1 + R_2 + \dots + R_N} = \frac{R_N v_s}{R_{eq}}$$
 (2.15)

2.8.2 The current-divider circuit

The current-divider circuit shown in Fig. 2.10. The current divider is designed to divide the current i_s between \mathbf{R}_1 and \mathbf{R}_2 . We find the relationship between the current i_s , and the current in each resistor (that is, i_1 and i_2) by directly applying **Ohm's law and Kirchhoff's current law**. The voltage across the parallel resistors is

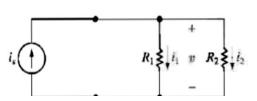


Figure 2.10 the current-divider circuit.

$$v = i_1 R_1 = i_2 R_2 = \frac{R_1 R_2}{R_1 + R_2} i_s$$

$$i_1 = \frac{R_2 i_s}{R_1 + R_2}, \quad i_2 = \frac{R_1 i_s}{R_1 + R_2}$$
(2.16)

If we divide both the numerator and denominator by R_1R_2 , Eq. (2.16) become

$$i_1 = \frac{G_1 i_s}{G_1 + G_2}, \quad i_2 = \frac{G_2 i_s}{G_1 + G_2}$$
 (2.17)

Thus, in general, if a current divider has N conductors $(G_1, G2, \ldots, G_N)$ in parallel with the source current i, the nth conductor (G_N) will have current

$$i_N = \frac{G_N i_s}{G_1 + G_2 + \dots + G_N} = \frac{R_{eq} i_s}{R_N}$$
 (2.18)

EXAMPLE 2.4: Find i_o and v_o in the circuit shown in **Fig. 1(a)**. Calculate the power dissipated in the 3- Ω resistor.

Solution: The 6- Ω and 3- Ω resistors are in parallel, so their combined resistance is

$$6 \Omega \parallel 3 \Omega = 6 \times 3/(6+3) = 2 \Omega$$

By apply voltage division, since the 12 V in **Fig. 1(b)** is divided between the 4- Ω and 2- Ω resistors. Hence,

$$v_o = 2(12 \text{ V})/(2 + 4) = 4 \text{ V}$$

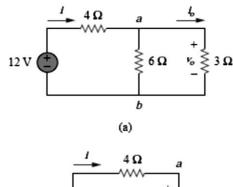
Apply current division to the circuit in **Fig. 1(a)** now that we know i, by writing

$$i = 12/4 + 2 = 2 \text{ A}$$

 $i_0 = 6 i/(6+3) = 4/3 \text{ A}$

The power dissipated in the 3- Ω resistor is

$$p_o = v_o i_o = 4(4/3) = 5.333 \text{ W}$$



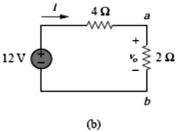
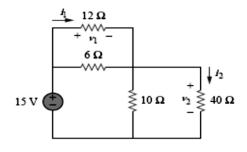


Figure 1(a) Original circuit, (b) Its equivalent circuit.

PRACTICE PROBLEM 2.4: Find v_1 and v_2 in the circuit shown in Figure below. Also calculate i_1 and i_2 and the power dissipated in the $12-\Omega$ and $40-\Omega$ resistors.

Answer: $v_I = 5 \text{ V}, i_I = 416.7 \text{ mA}, p_I = 2.083 \text{ W}, v_2 = 10 \text{ V}, i_2 = 250 \text{ mA}, p_2 = 2.5 \text{ W}.$



2.9 WYE-DELTA TRANSFORMATIONS

Situations often arise in circuit analysis when the resistors are neither in parallel nor in series. For example, consider the bridge circuit in **Fig. 2.11**. How do we combine resistors \mathbf{R}_1 through \mathbf{R}_6 when the resistors are neither in series nor in parallel? Many circuits of the type shown in **Fig. 2.11** can be simplified by using three-terminal equivalent networks. These are the wye (**Y**) or tee (**T**) network shown in **Fig. 2.12** and the delta (Δ) or pi (π) network shown in **Fig. 2.13**.

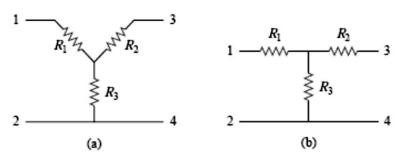


Figure 2.12 Two forms of the same network: (a) Y, (b) T.

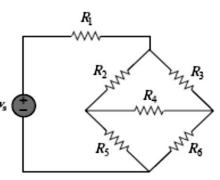


Figure 2.11The bridge network.

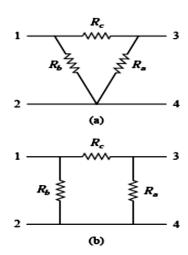


Figure 2.13 Two forms of the same network: (a) Δ , (b) π .

Delta to Wye Conversion

Suppose it is more convenient to work with a **wye** network in a place where the circuit contains a delta configuration. We superimpose a **wye** network on the existing **delta** network and find the equivalent resistances in the **wye** network. For terminals 1 and 2 in **Figs. 2.12** and **2.13**, for example, $\mathbf{R}_{12}(\mathbf{Y}) = \mathbf{R}\mathbf{1} + \mathbf{R}\mathbf{3}$, $\mathbf{R}_{12}(\mathbf{\Lambda}) = \mathbf{R}_{b} \parallel (\mathbf{R}_{a} + \mathbf{R}_{c})$ (2.19) Setting $\mathbf{R}_{12}(\mathbf{Y}) = \mathbf{R}_{12}(\mathbf{\Lambda})$ gives

$$R_{12} = R_1 + R_3 = \frac{R_b(R_a + R_c)}{R_a + R_b + R_c}$$

$$R_{13} = R_1 + R_2 = \frac{R_c(R_a + R_b)}{R_a + R_b + R_c}$$

$$R_{34} = R_2 + R_3 = \frac{R_a(R_b + R_c)}{R_a + R_b + R_c}$$
(2.20)

By solving previous equations, we get

$$\mathbf{R_1} = \frac{\mathbf{R_b} \, \mathbf{R_c}}{\mathbf{R_a} + \mathbf{R_b} + \mathbf{R_c}} \tag{2.21}$$

$$\mathbf{R_2} = \frac{\mathbf{R_c} \, \mathbf{R_a}}{\mathbf{R_a} + \mathbf{R_b} + \mathbf{R_c}} \tag{2.22}$$

$$\mathbf{R_3} = \frac{\mathbf{R_a} \, \mathbf{R_b}}{\mathbf{R_a} + \mathbf{R_b} + \mathbf{R_c}} \tag{2.23}$$

Wye to Delta Conversion

Reversing the Δ -to-Y transformation also is possible. That is, we can start with the Y structure and replace it with an equivalent Δ structure. The expressions for the three Δ -connected resistors as functions of the three Y-connected resistors are

$$Ra = \frac{R1R2 + R2R3 + R3R1}{R1}$$
 (2.24)

$$\mathbf{Rb} = \frac{\mathbf{R}\mathbf{1}\mathbf{R}\mathbf{2} + \mathbf{R}\mathbf{2}\mathbf{R}\mathbf{3} + \mathbf{R}\mathbf{3}\mathbf{R}\mathbf{1}}{\mathbf{R}\mathbf{2}}$$
 (2.25)

$$Rc = \frac{R1R2 + R2R3 + R3R1}{R3}$$
 (2.26)

The Y and Δ networks are said to be balanced when

$$\mathbf{R}_1 = \mathbf{R}_2 = \mathbf{R}_3 = \mathbf{R}_{V}, \, \mathbf{R}_a = \mathbf{R}_b = \mathbf{R}_c = \mathbf{R}_A$$
 (2.27)

Under these conditions, conversion formulas become

$$\mathbf{R}_{\mathbf{Y}} = \mathbf{R}_{\Lambda} / \mathbf{3} \text{ or } \mathbf{R}_{\Lambda} = \mathbf{3} \mathbf{R}_{\mathbf{Y}}$$
 (2.28)

EXAMPLE 2.5: Obtain the equivalent resistance $\mathbf{R_{ab}}$ for the circuit in **Fig. 1** and use it_to find current **i**.

Solution:

In this circuit, there are two **Y**-networks and one Δ -network. Transforming just one of these will simplify the circuit. If we convert the **Y**-network comprising the 5- Ω , 10- Ω , and 20- Ω resistors, we may select

$$R_1 = 10 \Omega$$
, $R_2 = 20 \Omega$, $R_3 = 5 \Omega$

Thus, from Eqs. (2.24) to (2.26) we have

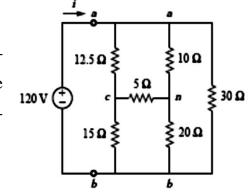


Figure 1.

Ra =
$$\frac{\text{R1R2} + \text{R2R3} + \text{R3R1}}{\text{R1}} = \frac{10 \times 20 + 20 \times 5 + 5 \times 10}{10} = \frac{350}{10} = 35 \Omega$$

Rb =
$$\frac{\text{R1R2} + \text{R2R3} + \text{R3R1}}{\text{R2}} = \frac{350}{20} = 17.5\Omega$$

$$\mathbf{Rc} = \frac{\mathbf{R1R2} + \mathbf{R2R3} + \mathbf{R3R1}}{\mathbf{R3}} = \frac{350}{5} = 70\Omega$$

With the Y converted to Δ , the equivalent circuit (with the voltage source removed for now) is shown in **Fig. 2** (a). Combining the three pairs of resistors in parallel, we obtain

$$70 \parallel 30 = 70 \times 30/(70 + 30) = 21 \Omega$$

12.5 || 17.5 = 12.5
$$\times$$
 17.5/ (12.5 + 17.5) = 7.2917 Ω

$$15 \parallel 35 = 15 \times 35/(15 + 35) = 10.5 \Omega$$

so that the equivalent circuit is shown in Fig. 2 (b). Hence, we find

$$R_{ab} = (7.292 + 10.5) \parallel 21 = 17.792 \times 21/(17.792 + 21) = 9.632 \Omega$$

Then

$$i = vs/Rab = 120/9.632 = 12.458 A$$

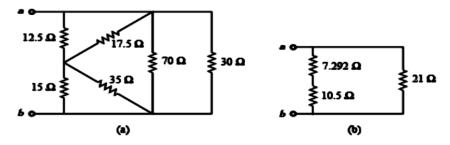
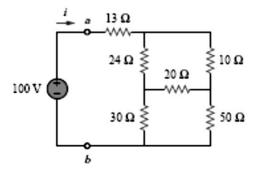


Figure 2 Equivalent circuits to Fig. 1, with the voltage removed.

PRACTICE PROBLEM 2.5: For the bridge network in Figure below, find \mathbf{R}_{ab} and \mathbf{i} .

Answer: 40Ω , 2.5 A.



2.10 Energy Sources

There are basically two types of energy sources; voltage source and current source. These sources are classified as i) Ideal source and ii) Practical source. Let us see the difference between Ideal and practical sources.

2.10.1 Voltage Source

*Ideal voltage source:

Ideal voltage source is defined as the energy source which gives constant voltage across its terminals irrespective of the current drawn through its terminals. This is indicated by V-I characteristics shown in the Fig. 2.14 (b).

*Practical voltage source:

But practically, every voltage source has small internal resistance shown in series with voltage source and is represented by \mathbf{R}_{se} as shown in the **Fig. 2.15**. Because of the \mathbf{R}_{se} , voltage across terminals decreases slightly with increase in current and it is given by expression,

$$V_L = V_S - I_L R_L$$

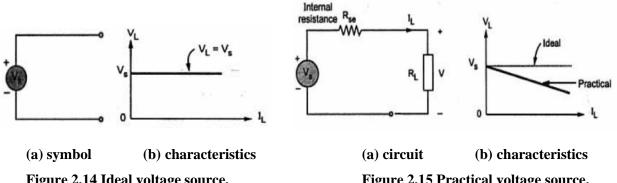


Figure 2.14 Ideal voltage source.

Figure 2.15 Practical voltage source.

Voltage sources are further classified as follows,

i) Time invariant Sources:

The sources in which voltage is not varying with time are known as time invariant voltage source or **D.C.** sources. These are denoted by capital letters. Such a source is represented in the Fig. 2.16 (a).

ii) Time Variant Source:

The sources in which voltage is varying with time are known as time variant voltage sources or A.C. sources. These are denoted by small letters. This is shown in the Fig. 2.16 (b).

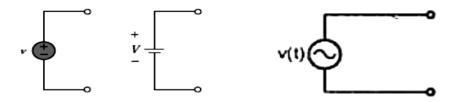


Figure 2.16 (a) D.C. sources.

Figure 2.16(b) A.C. source.

2.10.2 Current Source

*Ideal current source:

Ideal current source is the source which gives constant current at its terminals irrespective of the voltage appearing across its terminal. This is explained by V-I characteristics shown in the **Fig. 2.17** (b).

*Practical current source:

But practically, every current source has high internal resistance, shown in parallel with current source and It is represented by $\mathbf{R}_{\rm sh}$. This is shown in the **Fig. 2.18**. Because of $\mathbf{R}_{\rm sh}$, current through its terminals decreases slightly with voltage at its terminals.

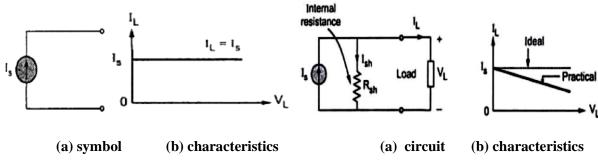


Figure 2.17 ideal current source.

Figure 2.18 ideal current source.

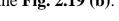
Similar to voltage sources, current sources are classified as follows,

i) Time Invariant Sources:

The sources in which current is not varying with time are known as time invariant current sources or D.C. sources. These are denoted by capital letters. Such a current source is represented in the **Fig. 2.19** (a).

ii) Time Variant Sources:

The sources in which current is varying with time are known as time variant current sources or **A.C. sources**. These are denoted by small letters. Such source is represented in the **Fig. 2.19** (b).



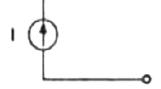


Figure 2.19 (a) D.C. source.

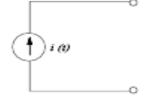


Fig. 2.19 (b) A.C. source.

The sources, which are discussed above are independent sources because these sources does not depend on other voltage or currents in the network for their value. These are represented by a circle with a polarity of voltage or direction of current indicated inside

2.10.3 Dependent Sources

Dependent source are those whose value of source depends on voltage or current in the circuit. Such sources are indicated by diamond as shown in the **Fig. 2.20** and further classified as,

- i) Voltage-Controlled Voltage Source (VCVS): It produces a voltage as a function of voltage elsewhere in the given circuit. It is shown in the **Fig. 2.20** (a). The controlling voltage is named v_x the equation that determines the supplied voltage v_s is
- $v_s = \mu v_x$, and the reference polarity for v_s is as indicated. Note that μ is a multiplying constant that is dimensionless.

ii) Current-Controlled Voltage Source (CCVS): It produces voltage as a function of current elsewhere in the given circuit. It is shown In the Fig. 2.20(b). the controlling current is i_x the equation for the supplied voltage v_s is $v_s = \rho i_x$,

the reference polarity is as shown and the multiplying constant ρ has the dimension volts per ampere

iii) Voltage-Controlled Current Source (VCCS): It produces current as a function of voltage elsewhere in the given circuit. It is shown in the **Fig. 2.20(c)**. The controlling voltage is v_x , the equation for the supplied current i_s is $i_s = \alpha v_x$,

the reference direction is as shown and the multiplying constant α has the dimension amperes per volt.

iv) Current-Controlled Current Source (CCCS): It produces current as a function of current elsewhere in the given circuit. It is shown in the Fig. 2.20 (d). the controlling current is i_x the equation for the supplied current i_s is $i_s = \beta i_x$.

the reference direction is as shown, and the multiplying constant β is dimensionless.

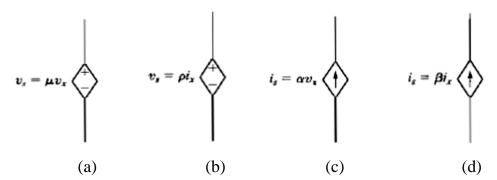


Figure 2.20 The circuit symbols a) an ideal dependent voltage-controlled voltage source, (b) an ideal dependent current-controlled voltages source, (c) an ideal dependent voltage-controlled current source (d) an ideal dependent current-controlled current source.

Dependent sources are useful in modeling elements such as transistors, operational amplifiers and integrated circuits. An example of a current controlled voltage source is shown on the right-hand side of **Fig. 2.21**, where the voltage 10i of the voltage source depends on the current *i* through element C.

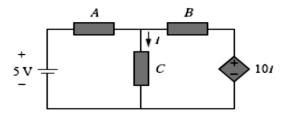


Figure 2.21 the source on the right-hand side is a current-controlled voltage source.

2.11 Combinations of Sources

In a network consisting of many sources, series and parallel combinations of sources exist. If such combinations are replaced by the equivalent source then the network simplification becomes much easier. Let us consider such series and parallel combinations of energy sources.

2.11.1 Voltage Sources in Series

If two voltage sources are in series then the equivalent is dependent on the polarities of the two sources. Consider the two sources as shown in the **Fig. 2.22**.

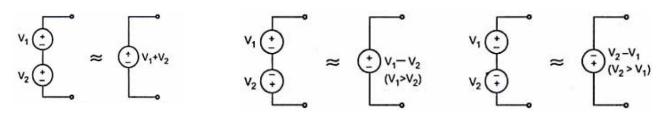


Figure 2.22 Figure 2.23

If the polarities of the two sources are same then the equivalent single source is the addition of the two sources with polarities same as that of the two sources.

Consider the two sources as shown in the **Fig. 2.23**. If the polarities of the two sources are different then the equivalent single source is the difference between the two voltage sources. The polarity of such source is same as that of the greater of the two sources.

Key Point: the voltage sources to be connected in series must have same current rating through their voltage ratings may be same or different.

2.11.2 Voltage Sources in Parallel

Consider the two voltage source in parallel as shown in the **Fig. 2.24**. The equivalent single source has a value same as V_1 and V_2 . It must be noted that all the open circuit voltage provided by each source must be equal as the sources are in parallel.

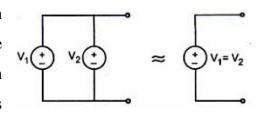
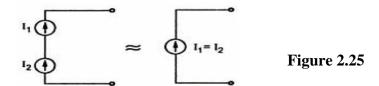


Figure 2.24

Key Point: the voltage sources to be connected in parallel must have same voltage rating through their current ratings may be same or different.

2.11.3 Current Sources in Series

Consider the two current sources in series is shown in the **Fig. 2.25**, the equivalent single source has a value same as I_1 and I_2 .



Key Point: the current sources to be connected in series must have same current rating through their voltage ratings may be same or different.

2.11.4 Current Sources in Parallel

Consider the two current sources in parallel as shown in the Fig. 2.26.

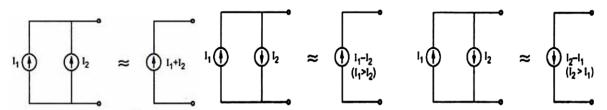


Figure 2.27

if the directions of the currents of the sources connected in parallel are same then the equivalent single source is the addition of the two sources with direction same as that of the two sources.

Consider the two current sources with opposite directions connected in parallel as shown in the **Fig. 2.27**. If the directions of the two sources are different then the equivalent single source has a direction same as greater of the two sources with value equal to the difference between the two voltage sources.

Key Point: the current sources to be connected in parallel must have same voltage rating through their current ratings may be same or different.

2.12 NOTATION: it will play an increasingly important role in the analysis.

i) **Double-Subscript Notation**

The fact that voltage is an across variable and exists between two points has resulted in a double-subscript notation that defines the first subscript as the higher potential. In **Fig. 2.28(a)**, the two points that define the voltage across the resistor **R** are denoted by **a** and **b**. Since a is the first subscript for V_{ab} , point a must have a higher potential than point **b** if V_{ab} is to have a positive value. If, in fact, point **b** is at a higher potential than point a, V_{ab} will have a negative value, as indicated in **Fig. 2.28(b)**.



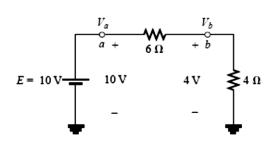
Figure 2.28 defining the sign for double-subscript notation.

In summary:

The voltage V_{ab} is the voltage at point a with respect to (w.r.t.) point b.

ii) Single-Subscript Notation

If point **b** of the notation V_{ab} is specified as ground potential (zero volts), then a single subscript notation can be employed that provides the voltage at a point with respect to ground.



In Fig. 2.29, V_a is the voltage from point a to ground. In this case it is obviously 10 V since it is $_{10V}$ $_{4V}$ $_{4V}$ $_{4\Omega}$ right across the source voltage E. The voltage V_b is

the voltage from point b to ground. Because it is ground. In this case it is obviously ${\bf 10}\ {\bf V}$ since it is the voltage from point **b** to ground. Because it is directly across the **4-\Omega** resistor, $V_b = 4 \text{ V}$.

Figure 2.29 defining the use of single-subscript notation for voltage levels.

In summary:

The single-subscript notation Va specifies the voltage at point a with respect to ground (zero volts). If the voltage is less than zero volts, a negative sign must be associated with the magnitude of Va.

General Comments

A particularly useful relationship can now be established that will have extensive applications in the analysis of electronic circuits. For the above notational standards, the following relationship exists:

$$V_{ab} = V_a - V_b \tag{2.29}$$

In other words, if the voltage at points **a** and **b** is known with respect to ground, then the voltage V_{ab} can be determined using Eq. (2.29). In Fig. 2.29, for example,

$$V_{ab} = V_a$$
 - $V_b = 10$ V- 4 V = 6 V

2.13 KIRCHHOFF'S LAWS

Ohm's law by itself is not sufficient to analyze circuits. However, when it is coupled with Kirchhoff's two laws, we have a sufficient, powerful set of tools for analyzing a large variety of electric circuits. Kirchhoff's laws were first introduced in 1847 by the German physicist Gustav Robert Kirchhoff (1824–1887). These laws are formally known as Kirchhoff's current law (**KCL**) and Kirchhoff's voltage law (**KVL**).

2.13.1 Kirchhoff's current law

Kirchhoff's current law (KCL) states that the algebraic sum of currents entering a node (or a closed boundary) is zero or the sum of the currents entering a node is equal to the sum of the currents leaving the node.

Mathematically, KCL implies that

$$\sum_{n=1}^{N} i_n = 0 \tag{2.30}$$

where N is the number of branches connected to the node and i_n is the nth current entering (or leaving) the node.

Consider the node in **Fig. 2.30**. Applying **KCL** gives

$$\mathbf{i}_1 + (-\mathbf{i}_2) + \mathbf{i}_3 + \mathbf{i}_4 + (-\mathbf{i}_5) = \mathbf{0}$$
 (2.31)

since currents i_1 , i_3 , and i_4 are entering the node, while currents i_2 and i_5 are leaving it. By rearranging the terms, we get

$$\mathbf{i}_1 + \mathbf{i}_3 + \mathbf{i}_4 = \mathbf{i}_2 + \mathbf{i}_5$$
 (2.32)

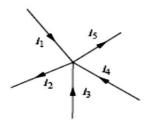


Figure 2.30 Currents at a node illustrating KCL.

A simple application of **KCL** is combining current sources in parallel. The combined current is the algebraic sum of the current supplied by the individual sources. For example, the current sources shown in **Fig. 2.31(a)** can be combined as in **Fig. 2.31(b)**. The combined or equivalent current source can be found by applying **KCL** to node **a**.

$$\mathbf{I}_{\mathrm{T}} + \mathbf{I}_{2} = \mathbf{I}_{1} + \mathbf{I}_{3}$$

or

$$\mathbf{I}_{\mathrm{T}} = \mathbf{I}_1 - \mathbf{I}_2 + \mathbf{I}_3$$

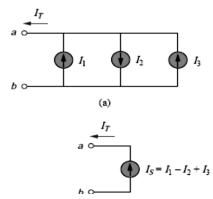


Figure 2.31 Current sources in parallel: (a) original circuit, (b) equivalent circuit.

A circuit cannot contain two different currents, I_1 and I_2 , in series, unless $I_1 = I_2$; otherwise **KCL** will be violated.

2.13.2 Kirchhoff's voltage law

Kirchhoff's second law is based on the principle of conservation of energy:

Kirchhoff's voltage law (KVL) states that the algebraic sum of all voltages around a closed path (or loop) is zero.

Expressed mathematically, KVL states that

$$\sum_{m=1}^{M} v_m = 0 \tag{2.33}$$

Where **M** is the number of voltages in the loop (or the number of branches in the loop) and v_m is the mth voltage.

To illustrate **KVL**, consider the circuit in **Fig. 2.32**. The sign on each voltage is the polarity of the terminal encountered first as we travel around the loop. We can start with any branch and go around the loop either clockwise or counterclockwise. Suppose we start with the voltage source and go clockwise around the loop as shown; then voltages would be $-\mathbf{v}_1$, $+\mathbf{v}_2$, $+\mathbf{v}_3$, $-\mathbf{v}_4$, and $+\mathbf{v}_5$, in that order. For example, as we reach branch 3, the positive terminal is met first; hence we have+ \mathbf{v}_3 . For branch 4, we reach the negative terminal first; hence, $-\mathbf{v}_4$. Thus, **KVL** yields

$$-\mathbf{v}_1 + \mathbf{v}_2 + \mathbf{v}_3 - \mathbf{v}_4 + \mathbf{v}_5 = \mathbf{0} \tag{2.34}$$

Rearranging terms gives

$$\mathbf{v}_2 + \mathbf{v}_3 + \mathbf{v}_5 = \mathbf{v}_1 + \mathbf{v}_4 \tag{2.35}$$

which may be interpreted as

Sum of voltage drops = Sum of voltage rises
$$(2.36)$$

This is an alternative form of **KVL**. Notice that if we had traveled counterclockwise, the result would have been $+\mathbf{v_1}$, $-\mathbf{v_5}$, $+\mathbf{v_4}$, $-\mathbf{v_3}$, and $-\mathbf{v_2}$, which is the same as before, except that the signs are reversed. Hence, **Eqs.** (2.34) and (2.35) remain the same.

When voltage sources are connected in series, **KVL** can be applied to obtain the total voltage. The combined voltage is the algebraic sum of the voltages of the individual sources.

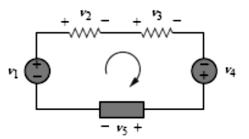


Figure 2.32 A single-loop circuit illustrating KVL.

2.13.3 Steps to Apply Kirchhoff. Laws to Get Network Equations

The steps are stated based on the branch current method.

Step 1: Draw the circuit diagram from the given information and insert all the value of sources with appropriate polarities and all the resistances.

Step 2: Mark all the branch currents with assumed directions using **KCL** at various nodes and junction points. Kept the number of unknown currents as minimum as far as possible to limit the mathematical calculations required to solve them later on. Assumed directions may be wrong; in such case answer of such current will be mathematically negative which indicates the correct direction of the current.

Step 3: Mark all the polarities of voltage drops and rises as per directions of the assumed branch currents flowing through various branch resistance of the network. This is necessary for application of **KVL** to various closed loops.

Step 4: Apply **KVL** to different closed paths in the network and obtain the corresponding equations. Each equation must contain some element which is not considered in any preview equation.

2.14 Solving Simultaneous Equations and Cramer's Rule

Electric circuit analysis with the help of Kirchhoff's laws usually involves solution of two or three simultaneous equations. These equations can be solved by a systematic elimination of the variables but the procedure is often lengthy and laborious and hence more liable to error. Determinants and Cramer's rule provide a simple and straight method for solving network equations through manipulation of their coefficients. Of course, if the number of simultaneous equations happens to be very large, use of a digital computer can make the task easy. Let us assume that set of simultaneous equations obtained is, as follows,

$$\begin{array}{c} a_{11} x_1 + a_{12} x_2 + \dots + a_{1n} x_n = C_1 \\ a_{21} x_1 + a_{22} x_2 + \dots + a_{2n} x_n = C_2 \\ & \vdots \\ a_{n1} x_1 + a_{n2} x_2 + \dots + a_{nn} x_n = C_n \end{array}$$

where C_1 , C_2 ,, C_n constants. Then Cramer's rule says that form a system determinant Δ or D as,

$$\Delta = \begin{bmatrix} a_{11} & a_{12} & \cdots & a_{1n} \\ a_{21} & a_{22} & \cdots & a_{2n} \\ \vdots & & & & \\ a_{n1} & a_{n2} & \cdots & a_{nn} \end{bmatrix} = D$$

Then obtain the subdeterminant Dj by replacing j^{th} column of Δ by the column of constants existing on right hand side of equations i.e. C_1 , C_2 , C_n ;

$$D_{1} = \begin{bmatrix} C_{1} & a_{12} & \cdots & a_{1n} \\ C_{n} & a_{22} & \cdots & a_{2n} \\ \vdots & & & \vdots \\ C_{n} & a_{n2} & \cdots & a_{nn} \end{bmatrix}, \qquad D_{2} = \begin{bmatrix} a_{11} & C_{1} & \cdots & a_{1n} \\ a_{21} & C_{2} & \cdots & a_{2n} \\ \vdots & & & \vdots \\ a_{n1} & C_{n} & \cdots & a_{nn} \end{bmatrix}$$

$$D_{n} = \begin{bmatrix} a_{11} & a_{12} & \cdots & C_{1} \\ a_{21} & a_{22} & \cdots & C_{2} \\ \vdots & & & \vdots \\ a_{n1} & a_{n2} & \cdots & C_{n} \end{bmatrix}$$

and

The unknowns of the equations are given by Cramer's rule as,

$$X_1 = \frac{D_1}{D}, \quad X_2 = \frac{D_2}{D}, \dots, X_n = \frac{D_n}{D}$$

Where $D_1, D_2, ..., D_n$ and D are values of the respective determents

Example 2.6: Apply Kirchhoff's laws to the circuit shown in figure 1 below Indicate the various branch currents.

Write down the equations relating the various branch currents.

Solve these equations to find the values of these currents.

Is the sign of any of the calculated currents negative?

If yes, explain the significance of the negative sign.

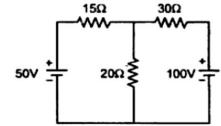


Figure 1

Solution: Application Kirchhoff's laws:

Step 1and 2: Draw the circuit with all the values which are same as the given network.

Mark all the branch currents starting from +ve of any of the source, say +ve of 50 V source

Step 3: Mark all the polarities for different voltages across the resistance. This is combined with step 2 shown in the network below in Fig. 1 (a).

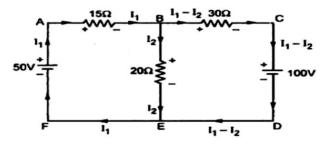


Figure 1 (a)

Step 4: Apply KVL to different loops.

Loop 1: A-B-E-F-A,
$$-15 I_1 - 20 I_2 + 50 = 0$$

Loop 2: **B-C-D-E-D**,
$$-30 (I_1 - I_2) - 100 + 20 I_2 = 0$$

Rewriting all the equations, taking constants on one side,

15
$$I_1 + 20 I_2 = 50$$
, $-30 I_1 + 50 I_2 = 100$

Apply Cramer's rule,
$$D = \begin{vmatrix} 15 & 20 \\ -30 & 50 \end{vmatrix} = 1350$$

Calculating
$$D_1$$
, $D_1 = \begin{vmatrix} 50 & 20 \\ 100 & 50 \end{vmatrix} = 500$

$$I_1 = \frac{D_1}{D} = \frac{500}{1350} = 0.37 A$$

Calculating
$$D_2$$
, $D_2 = \begin{vmatrix} 15 & 50 \\ -30 & 100 \end{vmatrix} = 3000$

$$I_2 = \frac{D_2}{D} = \frac{3000}{1350} = 2.22 A$$

For I_1 and I_2 as answer is positive, assumed direction is correct.

For I_1 answer is **0.37** A. For I_2 answer is **2.22** A

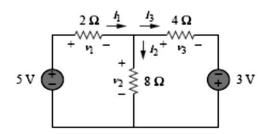
$$I_1 - I_2 = 0.37 - 2.22 = -1.85 A$$

Negative sign indicates assumed direction is wrong.

i.e. $I_1 - I_2 = 1.85$ A flowing in opposite direction to that of the assumed direction.

Practice problem 2.6: Find the currents and voltages in the circuit shown below.

Answer:
$$v_1 = 3 \text{ V}$$
, $v_2 = 2 \text{ V}$, $v_3 = 5 \text{ V}$, $i_1 = 1.5 \text{ A}$, $i_2 = \cdot .25 \text{ A}$, $i_3 = 1.25 \text{ A}$.



2.15 SOURCE TRANSFORMATION

We have noticed that series-parallel combination and wye-delta transformation help simplify circuits. Source transformation is another tool for simplifying circuits. We can substitute a voltage source in series with a resistor for a current source in parallel with a resistor, or vice versa, as shown in **Fig. 2.33**. Either substitution is known as a *source transformation*.

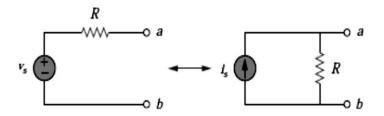


Figure 2.33 Transformation of independent sources.

Key Point: A source transformation is the process of replacing a voltage source v_s in series with a resistor R by a current source is in parallel with a resistor R, or vice versa.

We need to find the relationship between v_s and i_s that guarantees the two configurations in Fig. 2.33 are equivalent with respect to nodes a, b.

Suppose R_L , is connected between nodes a, b in Fig.2.33(a). Using Ohms law, the current in R_L is.

$$i_L = \frac{v_s}{(R+R_L)}$$
 R and R_L in series (2.37)

If it is to be replaced by a current source then load current must be $\frac{V}{(R+R_L)}$

Now suppose the same resistor \mathbf{R}_{L} , is connected between nodes \mathbf{a} , \mathbf{b} in Fig. 4.4 (b). Using current division, the current in \mathbf{R}_{L} , is

$$i_L = i_s \frac{R}{(R+R_L)} \tag{2.38}$$

If the two circuits in **Fig. 4.4** are equivalent, these resistor currents must be the same. Equating the right-hand sides of **Eqs.4.5** and **4.6** and simplifying

$$\mathbf{i}_s = \frac{\mathbf{v}_s}{\mathbf{p}} \quad or \quad \mathbf{v}_s = \mathbf{i}_s \, \mathbf{R} \tag{2.39}$$

Source transformation also applies to dependent sources, provided we carefully handle the dependent variable. As shown in **Fig.2.34**, a dependent voltage source in series with a resistor can be transformed to a dependent current source in parallel with the resistor or vice versa.

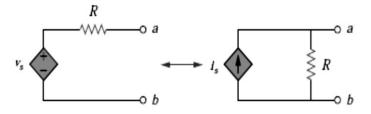


Figure 2.34 Transformation of dependent sources.

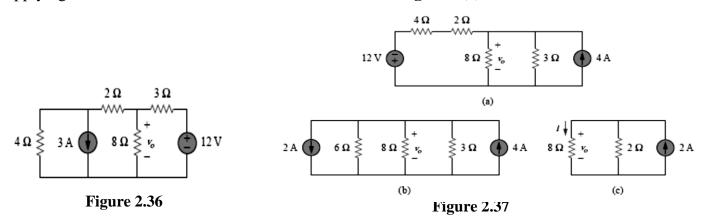
However, we should keep the following points in mind when dealing with source transformation.

- 1. Note from Fig. 2.33 (or Fig. 2.34) that the arrow of the current source is directed toward the positive terminal of the voltage source.
- 2. Note from Eq. (2.39) that source transformation is not possible when $\mathbf{R} = \mathbf{0}$, which is the case with an ideal voltage source. However, for a practical, nonideal voltage source, $\mathbf{R} \neq \mathbf{0}$. Similarly, an ideal current source with $\mathbf{R} = \infty$ cannot be replaced by a finite voltage source.

Example 2.7: Use source transformation to find v_o in the circuit in **Fig. 2.35**.

Solution:

We first transform the current and voltage sources to obtain the circuit in **Fig. 2.37(a)**. Combining the 4- Ω and 2- Ω resistors in series and transforming the 12-V voltage source gives us **Fig. 2.37(b)**. We now combine the 3- Ω and 6- Ω resistors in parallel to get 2- Ω . We also combine the 2-A and 4-A current sources to get a 2-A source. Thus, by repeatedly applying source transformations, we obtain the circuit in **Fig. 2.37 (c)**.



Alternatively, since the 8- Ω and 2- Ω resistors in **Fig. 2.37(c)** are in parallel, they have the same voltage vo across them. Hence,

$$v_o = (8||2)(2 \text{ A}) = \frac{8 \times 2}{10} (2) = 3.2 \text{ V}$$



CHAPTER THREE

METHODS OF ANALYSIS

3.1 INTRODUCTION

Having understood the fundamental laws of circuit theory (**Ohm's law** and **Kirchhoff's laws**), we are now prepared to apply these laws to develop two powerful techniques for circuit analysis: nodal analysis, which is based on a systematic application of Kirchhoff's current law (**KCL**), and mesh analysis, which is based on a systematic application of Kirchhoff's voltage law (**KVL**). The two techniques are so important that this chapter should be regarded as the most important in the lectures.

3.2 NODAL ANALYSIS

Nodal analysis provides a general procedure for analyzing circuits using node voltages as the circuit variables. Choosing node voltages instead of element voltages as circuit variables is convenient and reduces the number of equations one must solve simultaneously. To simplify matters, we shall assume in this section that circuits do not contain voltage sources. Circuits that contain voltage sources will be analyzed in the next section.

Steps to Determine Node Voltages:

- 1. Select a node as the reference node. Assign voltages v_1 , v_2 ,... v_{n-1} to the remaining n-1 nodes. The voltages are referenced with respect to the reference node.
- 2. Apply **KCL** to each of the **n-1** nonreference nodes. Use Ohm's law to express the branch currents in terms of node voltages.
- 3. Solve the resulting simultaneous equations to obtain the unknown node voltages.

We shall now explain and apply these three steps.

The first step in nodal analysis is selecting a node as the reference or datum node. The reference node is commonly called the ground since it is assumed to have zero potential. A reference node is indicated by any of the three symbols in **Fig. 3.1**. We shall always use the symbol in **Fig. 3.1(b)**. Once we have selected a reference node, we assign voltage designations to nonreference nodes. Consider, for example, the circuit in **Fig. 3.2(a)**. Node 0 is the reference node ($\mathbf{v} = \mathbf{0}$), while nodes 1 and 2 are assigned voltages \mathbf{v}_1 and \mathbf{v}_2 ,



respectively. Keep in mind that the node voltages are defined with respect to the reference node. As illustrated in **Fig. 3.2(a)**, each node voltage is the voltage with respect to the reference node.

The number of nonreference nodes is equal to the number of independent equations that we will derive.

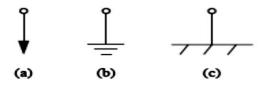


Figure 3.1 Common symbols for indicating a reference node.

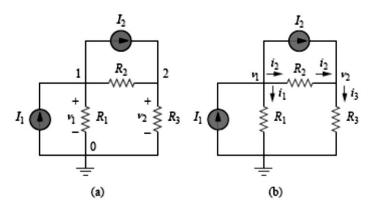


Figure 3.2 Typical circuits for nodal analysis.

As the second step, we apply **KCL** to each nonreference node in the circuit. To avoid putting too much information on the same circuit, the circuit in **Fig. 3.2(a)** is redrawn in **Fig. 3.2(b)**, where we now add i_1 , i_2 , and i_3 as the currents through resistors R_1 , R_2 , and R_3 , respectively. At node 1, applying **KCL** gives

$$\mathbf{I}_1 = \mathbf{I}_2 + \mathbf{i}_1 + \mathbf{i}_2 \tag{3.1}$$

At node 2,

$$\mathbf{I}_2 + \mathbf{i}_2 = \mathbf{i}_3 \tag{3.2}$$

We now apply Ohm's law to express the unknown currents i_1 , i_2 , and i_3 in terms of node voltages.

Current flows from a higher potential to a lower potential in a resistor.

We can express this principle as

$$i = \frac{v_{higher} - v_{lower}}{p} \tag{3.3}$$

Note that this principle is in agreement with the way we defined resistance in Chapter 2 (see **Fig. 2.3**). With this in mind, we obtain from **Fig. 3.2(b)**,



$$i_1 = \frac{v_1 - 0}{R_1}$$
,

$$i_2 = \frac{\mathbf{v}_1 - \mathbf{v}_2}{R_2},$$

$$i_3 = \frac{v_2 - 0}{R_2} , \qquad (3.4)$$

Substituting Eq. (3.4) in Eqs. (3.1) and (3.2) results, respectively, in

$$I_1 = I_2 + \frac{\mathbf{v}_1}{R_1} + \frac{\mathbf{v}_1 - \mathbf{v}_2}{R_2} \tag{3.5}$$

$$I_2 + \mathbf{v}_1 - \frac{\mathbf{v}_2}{R_2} = \frac{\mathbf{v}_2}{R_3} \tag{3.6}$$

In terms of the conductances, Eqs. (3.5) and (3.6) become

$$\mathbf{I}_{1} = \mathbf{I}_{2} + \mathbf{G}_{1}\mathbf{v}_{1} + \mathbf{G}_{2}(\mathbf{v}_{1} - \mathbf{v}_{2}) \tag{3.7}$$

$$\mathbf{I}_2 + \mathbf{G}_2 (\mathbf{v}_1 - \mathbf{v}_2) = \mathbf{G}_3 \mathbf{v}_2 \tag{3.8}$$

The third step in nodal analysis is to solve for the node voltages. If we apply **KCL** to n-1 nonreference nodes, we obtain n-1 simultaneous equations such as **Eqs.** (3.5) and (3.6) or (3.7) and (3.8). For the circuit of **Fig. 3.2**, we solve **Eqs.** (3.5) and (3.6) or (3.7) and (3.8) to obtain the node voltages v_1 and v_2 using any standard method, such as the substitution method, the elimination method, Cramer's rule, or matrix inversion. To use either of the last two methods, one must cast the simultaneous equations in matrix form. For example, **Eqs.** (3.7) and (3.8) can be cast in matrix form as

$$\begin{bmatrix} \mathbf{G_1} + \mathbf{G_2} & -\mathbf{G_2} \\ -\mathbf{G_2} & \mathbf{G_2} + \mathbf{G_3} \end{bmatrix} \begin{bmatrix} \mathbf{v_1} \\ \mathbf{v_2} \end{bmatrix} = \begin{bmatrix} \mathbf{I_1} - \mathbf{I_2} \\ \mathbf{I_2} \end{bmatrix}$$
(3.9)

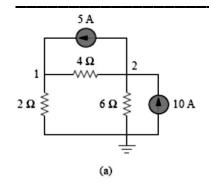
which can be solved to get v_1 and v_2 .

Example 3.1: Calculate the node voltages in the circuit shown in **Fig. 3.3(a)**.

Solution:

Consider **Fig. 3.3(b)**, where the circuit in **Fig. 3.3(a)** has been prepared for nodal analysis. Notice how the currents are selected for the application of **KCL**. Except for the branches with current sources, the labeling of the currents is arbitrary but consistent. (By consistent, we mean that if, for example, we assume that i2 enters the 4_resistor from the left-hand side, i2 must leave the resistor from the right-hand side.) The reference node is selected, and the node voltages v1 and v2 are now to be determined.





At node 1, applying KCL and Ohm's law gives

$$\mathbf{i}_1 = \mathbf{i}_2 + \mathbf{i}_3 \Rightarrow \mathbf{5} = \frac{v1 - v2}{4} + \frac{v1 - 0}{2}$$

Multiplying each term in the last equation by 4, we obtain

$$20 = v_1 - v_2 + 2v_1$$

or

$$3\mathbf{v}_1 - \mathbf{v}_2 = 20 \tag{3.1.1}$$

At node 2, we do the same thing and get

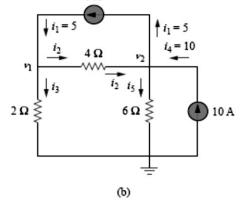
$$i_2 + i_4 = i_1 + i_5 \Rightarrow \frac{v_1 - v_2}{4} + 10 = 5 + \frac{v_2 - 0}{6}$$

Multiplying each term by 12 results in

$$3v_1 - 3v = +120 = 60 + 2v_2$$

or

$$-3v_1 + 5v_2 = 60 (3.1.2)$$



5 A

Figure 3.3 For Example 3.1: (a) original circuit, (b) circuit for analysis

Now we have two simultaneous **Eqs.** (3.1.1) and (3.1.2). We can solve the equations using any method and obtain the values of v1 and v2.

METHOD 1: Using the elimination technique, we add Eqs. (3.1.1) and (3.1.2).

$$4v_2 = 80 \Rightarrow v_2 = 20 \text{ V}$$

Substituting $v_2 = 20$ in Eq. (3.1.1) gives

$$3v_1 - 20 = 20 \Rightarrow v_1 = 40/3 = 13.33 \text{ V}$$

METHOD 2: To use Cramer's rule, we need to put Eqs. (3.1.1) and (3.1.2) in matrix form as

$$\begin{bmatrix} 3 & -1 \\ -3 & 5 \end{bmatrix} \begin{bmatrix} v_1 \\ v_2 \end{bmatrix} = \begin{bmatrix} 20 \\ 60 \end{bmatrix} \tag{3.1.3}$$

The determinant of the matrix is

$$\Delta = D = \begin{vmatrix} 3 & -1 \\ -3 & 5 \end{vmatrix} = 15 - 3 = 12$$

We now obtain $\mathbf{v_1}$ and $\mathbf{v_2}$ as

$$v_1 = \frac{D_1}{D} = \frac{\begin{vmatrix} 20 & -1 \\ 60 & 5 \end{vmatrix}}{D} = \frac{100 + 60}{12} = 13.33V$$

$$v_2 = \frac{D_2}{D} = \frac{\begin{vmatrix} 3 & 20 \\ -3 & 60 \end{vmatrix}}{D} = \frac{180 + 60}{12} = 20V$$



If we need the currents, we can easily calculate them from the values of the nodal voltages.

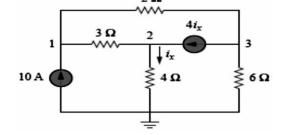
$$i_1 = 5 \text{ A}, i_2 = \frac{v_1 - v_2}{4} = -1.6667 \text{ A}, i_3 = \frac{v_1}{2} = 6.666 \text{ A}, i_4 = 10 \text{ A}, i_5 = \frac{v_2}{6} = 3.333 \text{ A}$$

The fact that i_2 is negative shows that the current flows in the direction opposite to the one assumed.

Practice problem 3.1: Find the voltages at the three nonreference nodes in the circuit of

Figure below.

Answer: $v_1 = 80 \text{ V}, v_2 = -64 \text{ V}, v_3 = 156 \text{ V}.$



3.2.1 NODAL ANALYSIS WITH VOLTAGE SOURCES

We now consider how voltage sources affect nodal analysis. We use the circuit in **Fig. 3.4** for illustration. Consider the following two possibilities.

CASE 1: If a voltage source is connected between the reference node and a nonreference node, we simply set the voltage at the nonreference node equal to the voltage of the voltage source. In **Fig. 3.4**, for example,

$$\mathbf{v_1} = \mathbf{10} \ \mathbf{V} \tag{3.10}$$

Thus our analysis is somewhat simplified by this knowledge of the voltage at this node.

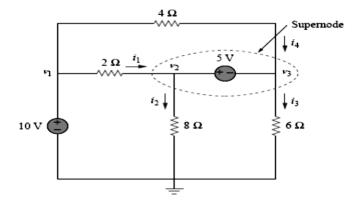


Figure 3.4 A circuit with a supernode.

<u>CASE 2:</u> If the voltage source (dependent or independent) is connected between two nonreference nodes, the two nonreference nodes form a generalized node or supernode; we apply both **KCL** and **KVL** to determine the node voltages.

A supernode is formed by enclosing a (dependent or independent) voltage source connected between two nonreference nodes and any elements connected in parallel with it.



In **Fig. 3.4**, nodes 2 and 3 form a supernode. (We could have more than two nodes forming a single supernode. For example, see the circuit in the **Practice problem 3.4**). We analyze a circuit with supernodes using the same three steps mentioned in the previous section except that the supernodes are treated differently. Why? Because an essential component of nodal analysis is applying **KCL**, which requires knowing the current through each element. There is no way of knowing the current through a voltage source in advance. However, **KCL** must be satisfied at a supernode like any other node. Hence, at the supernode in **Fig. 3.5**,

$$\mathbf{i_1} + \mathbf{i_4} = \mathbf{i_2} + \mathbf{i_3}$$
 (3.11a)

or $\frac{v_1 - v_2}{2} + \frac{v_1 - v_3}{4} = \frac{v_2 - 0}{8} + \frac{v_3 - 0}{6}$ (3.11b)

To apply Kirchhoff's voltage law to the supernode in **Fig. 3.4**, we redraw the circuit as shown in **Fig. 3.5**. Going around the loop in the clockwise direction gives

$$-\mathbf{v}_2 + \mathbf{5} + \mathbf{v}_3 = \mathbf{0} \Rightarrow \mathbf{v}_2 - \mathbf{v}_3 = \mathbf{5} \tag{3.12}$$

From **Eqs.** (3.10), (3.11b), and (3.12), we obtain the node voltages.

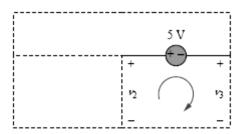


Figure 3.5 Applying KVL to a supernode.

Example 3.2: For the circuit shown in **Fig. 3.6**, find the node voltages.

Solution:

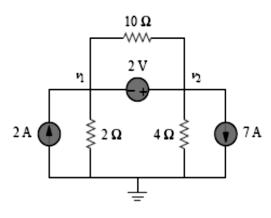


Figure 3.6 For Example 3.2.

The supernode contains the 2-V source, nodes 1 and 2, and the $10-\Omega$ resistor. Applying KCL to the supernode as shown in Fig. 3.7(a) gives

$$2 = i_1 + i_2 + 7$$

Expressing i_1 and i_2 in terms of the node voltages

$$2 = \frac{v1-0}{2} + \frac{v2-0}{4} + 7$$

or

$$\mathbf{v}_2 = -20 - 2\mathbf{v}_1 \tag{3.2.1}$$



To get the relationship between v_1 and v_2 , we apply **KVL** to the circuit in **Fig. 3.7(b)**. Going around the loop, we obtain

$$-\mathbf{v}_1 - 2 + \mathbf{v}_2 = \mathbf{0} \implies \mathbf{v}_2 = \mathbf{v}_1 + \mathbf{2}$$
 (3.3.2)

From **Eqs.** (3.2.1) and (3.2.2), we write

$$\mathbf{v}_2 = \mathbf{v}_1 + 2 = -20 - 2\mathbf{v}_1$$

or

$$3v_1 = -22 \implies v_1 = -7.333 \text{ V}$$

and $\mathbf{v_2} = \mathbf{v_1} + \mathbf{2} = -5.333$ V. Note that the 10- Ω resistor does not make any difference because it is connected across the supernode.

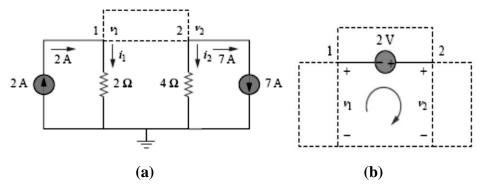
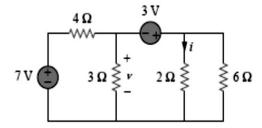


Figure 3.7 Applying: (a) KCL to the supernode, (b) KVL to the loop.

Practice problem 3.2: Find **v** and **i** in the circuit in Figure below.

Answer: -0.2 V, 1.4 A.



3.3 MESH ANALYSIS

Mesh analysis provides another general procedure for analyzing circuits, using mesh currents as the circuit variables. Using mesh currents instead of element currents as circuit variables is convenient and reduces the number of equations that must be solved simultaneously. Recall that a loop is a closed path with no node passed more than once. A mesh is a loop that does not contain any other loop within it.

Nodal analysis applies **KCL** to find unknown voltages in a given circuit, while mesh analysis applies **KVL** to find unknown currents. Mesh analysis is not quite as general as nodal analysis because it is only applicable to a circuit that is planar. A **planar** circuit is one



that can be drawn in a plane with no branches crossing one another; otherwise it is nonplanar. A circuit may have crossing branches and still be planar if it can be redrawn such that it has no crossing branches. For example, the circuit in Fig. 3.8 (a) has two crossing branches, but it can be redrawn as in Fig. 3.8 (b). Hence, the circuit in Fig. 3.8 (a) is planar. However, the circuit in Fig. 3.9 is nonplanar, because there is no way to redraw it and avoid the branches crossing. Nonplanar circuits can be handled using nodal analysis, but they will not be considered in this text.

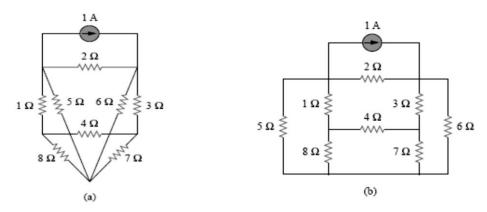


Figure 3.8 (a) A planar circuit with crossing branches, (b) the same circuit redrawn with no crossing branches.

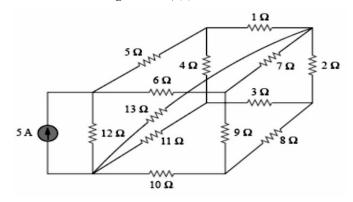


Figure 3.9 A nonplanar circuit.

To understand mesh analysis, we should first explain more about what we mean by a mesh. A mesh is a loop which does not contain any other loops within it.

In **Fig. 3.10**, for example, paths **abefa** and **bcdeb** are meshes, but path **abcdefa** is not a mesh. The current through a mesh is known as mesh current. In mesh analysis, we are interested in applying **KVL** to find the mesh currents in a given circuit.



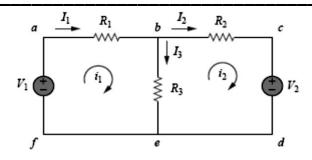


Figure 3.10 circuit with two meshes.

In this section, we will apply mesh analysis to planar circuits that do not contain current sources. In the next sections, we will consider circuits with current sources. In the mesh analysis of a circuit with \mathbf{n} meshes, we take the following three steps.

Steps to Determine mesh currents:

- 1. Assign mesh currents i_1, i_2, \ldots, i_n to the n meshes.
- 2. Apply **KVL** to each of the n meshes. Use Ohm's law to express the voltages in terms of the mesh currents.
- 3. Solve the resulting n simultaneous equations to get the mesh currents.

To illustrate the steps, consider the circuit in **Fig. 3.10**. The first step requires that mesh currents i_1 and i_2 are assigned to meshes 1 and 2. Although a mesh current may be assigned to each mesh in an arbitrary direction, it is conventional to assume that each mesh current flows clockwise.

As the second step, we apply **KVL** to each mesh. Applying **KVL** to mesh 1, we obtain

$$-\mathbf{V}_1 + \mathbf{R}_1 \mathbf{i}_1 + \mathbf{R}_3 (\mathbf{i}_1 - \mathbf{i}_2) = 0$$

or

$$(\mathbf{R}_1 + \mathbf{R}_3) \,\mathbf{i} \mathbf{1} - \mathbf{R}_3 \mathbf{i}_2 = \mathbf{V}_1 \tag{3.13}$$

For mesh 2, applying KVL gives

$$\mathbf{R}_2\mathbf{i}_2 + \mathbf{V}_2 + \mathbf{R}_3(\mathbf{i}_2 - \mathbf{i}_1) = \mathbf{0}$$

or

$$-\mathbf{R}_{3}\mathbf{i}_{1} + (\mathbf{R}_{2} + \mathbf{R}_{3}) \,\mathbf{i}_{2} = -\mathbf{V}_{2} \tag{3.14}$$

Note in Eq. (3.13) that the coefficient of i_1 is the sum of the resistances in the first mesh, while the coefficient of i_2 is the negative of the resistance common to meshes 1 and 2. Now observe that the same is true in Eq. (3.14). This can serve as a shortcut way of writing the mesh equations.



The third step is to solve for the mesh currents. Putting Eqs. (3.13). and (3.14) in matrix form yields

$$\begin{bmatrix} R_1 + R_3 & -R_3 \\ -R_3 & R_2 + R_3 \end{bmatrix} \begin{bmatrix} i_1 \\ i_2 \end{bmatrix} = \begin{bmatrix} V_1 \\ -V_2 \end{bmatrix}$$
(3.15)

which can be solved to obtain the mesh currents i_1 and i_2 . We are at liberty to use any technique for solving the simultaneous equations. If a circuit has \mathbf{n} nodes, \mathbf{b} branches, and \mathbf{l} independent loops or meshes, then $\mathbf{l} = \mathbf{b} - \mathbf{n} + \mathbf{l}$. Hence, \mathbf{l} independent simultaneous equations are required to solve the circuit using mesh analysis.

Notice that the branch currents are different from the mesh currents unless the mesh is isolated. To distinguish between the two types of currents, we use i for a mesh current and I for a branch current. The current elements I_1 , I_2 , and I_3 are algebraic sums of the mesh currents. It is evident from Fig. 3.13 that

$$I_1 = i_1, \quad I_2 = i_2, \quad I_3 = i_1 - i_2$$
 (3.16)

Example 3.3: For the circuit in **Fig. 3.11**, find the branch currents I_1 , I_2 , and I_3 using mesh analysis.

Solution:

We first obtain the mesh currents using **KVL**. For mesh 1,

$$-15 + 5i_1 + 10(i_1 - i_2) + 10 = 0$$

or

$$3i_1 - 2i_2 = 1 \tag{3.5.1}$$

For mesh 2,

$$6i_2 + 4i_2 + 10(i_2 - i_1) - 10 = 0$$

or

$$i_1 = 2i_2 - 1$$
 (3.5.2)

Using the substitution method, we substitute Eq. (3.3.2) into Eq. (3.3.1), and write

$$6i_2 - 3 - 2i_2 = 1 \Rightarrow i_2 = 1 A$$

From Eq. (3.5.2), $i_1 = 2i_2 - 1 = 2 - 1 = 1$ A. Thus,

$$I_1 = i_1 = 1 A$$
, $I_2 = i_2 = 1 A$, $I_3 = i_1 - i_2 = 0$

CHAPTER THREE METHODS OF ANALYSIS

Figure 3.11 For Example 3.3.

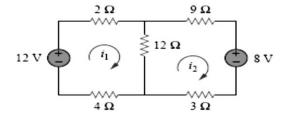
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<u>Practice problem 3.3:</u> Calculate the mesh currents i_1 and i_2 in the circuit of Figure below.

Answer: $i_1 = 2/3$ A, $i_2 = 0$ A.



3.3.1 MESH ANALYSIS WITH CURRENT

SOURCES

Applying mesh analysis to circuits containing current sources (dependent or independent) may appear complicated. But it is actually much easier than what we encountered in the previous section, because the presence of the current sources reduces the number of equations. Consider the following two possible cases.

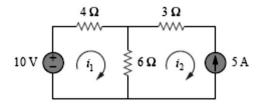


Figure 3.12 A circuit with a current source.

<u>CASE 1:</u> When a current source exists only in one mesh: Consider the circuit in **Fig. 3.12**, for example. We set $\mathbf{i_2} = -5$ A and write a mesh equation for the other mesh in the usual way, that is,

$$-10 + 4i_1 + 6(i_1 - i_2) = 0 \Rightarrow i_1 = -2 \text{ A}$$
(3.17)

<u>CASE 2:</u> When a current source exists between two meshes: Consider the circuit in **Fig. 3.13(a)**, for example. We create a supermesh by excluding the current source and any elements connected in series with it, as shown in **Fig. 3.13(b)**. Thus,

A supermesh results when two meshes have a (dependent or independent) current source in common.

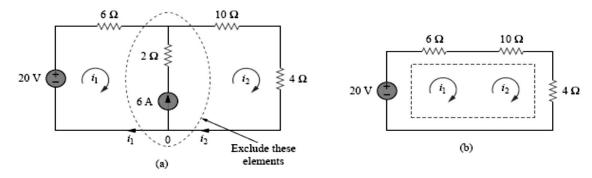


Figure 3.13 (a) Two meshes having a current source in common, (b) a supermesh, created by excluding the current source.



As shown in **Fig. 3.13(b)**, we create a supermesh as the periphery of the two meshes and treat it differently. (If a circuit has two or more supermeshes that intersect, they should be combined to form a larger supermesh.) Why treat the supermesh differently? Because mesh analysis applies **KVL**—which requires that we know the voltage across each branch—and we do not know the voltage across a current source in advance. However, a supermesh must satisfy **KVL** like any other mesh.

Therefore, applying **KVL** to the supermesh in **Fig. 3.13(b)** gives

$$-20 + 6i_1 + 10i_2 + 4i_2 = 0$$

or

$$6i_1 + 14i_2 = 20 (3.18)$$

We apply **KCL** to a node in the branch where the two meshes intersect.

Applying KCL to node 0 in Fig. 3.13(a) gives

$$\mathbf{i}_2 = \mathbf{i}_1 + \mathbf{6} \tag{3.19}$$

Solving **Eqs.** (3.18) and (3.19), we get

$$i_1 = -3.2 \text{ A}, i_2 = 2.8 \text{ A}$$
 (3.20)

Example 3.4: For the circuit in **Fig. 3.14**, find i_1 to i_4 using mesh analysis.

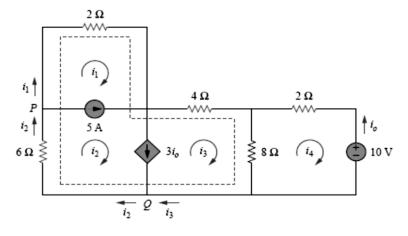


Figure 3.14 For Example 3.4.

Solution:

Note that meshes 1 and 2 form a supermesh since they have an independent current source in common. Also, meshes 2 and 3 form another supermesh because they have a dependent current source in common. The two supermeshes intersect and form a larger supermesh as shown. Applying **KVL** to the larger supermesh,

$$2i_1 + 4i_3 + 8(i_3 - i_4) + 6i_2 = 0$$

or

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$$\mathbf{i_1} + 3\mathbf{i_2} + 6\mathbf{i_3} - 4\mathbf{i_4} = \mathbf{0} \tag{3.4.1}$$

For the independent current source, we apply **KCL** to node P:

$$\mathbf{i}_2 = \mathbf{i}_1 + \mathbf{5} \tag{3.4.2}$$

For the dependent current source, we apply KCL to node Q:

$$\mathbf{i}_2 = \mathbf{i}_3 + 3\mathbf{i}_0$$

But $\mathbf{i_0} = -\mathbf{i_4}$, hence,

$$\mathbf{i}_2 = \mathbf{i}_3 - 3\mathbf{i}_4 \tag{3.4.3}$$

Applying KVL in mesh 4,

$$2i_4 + 8(i_4 - i_3) + 10 = 0$$

or

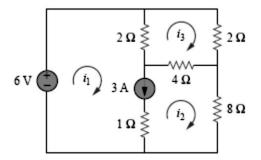
$$5i_4 - 4i_3 = -5 \tag{3.4.4}$$

From **Eqs.** (3.4.1) to (3.4.4),

$$i_1 = -7.5 \text{ A}, i_2 = -2.5 \text{ A}, i_3 = 3.93 \text{ A}, i_4 = 2.143 \text{ A}$$

<u>Practice problem 3.4:</u> Use mesh analysis to determine i_1 , i_2 , and i_3 in Figure shown below.

Answer: $i_1 = 3.474 \text{ A}$, $i_2 = 0.474 \text{ A}$, $i_3 = 1.105 \text{ A}$.





CHAPTER FOUR

CIRCUIT THEOREMS

4.1 INTRODUCTION

The growth in areas of application of electric circuits has led to an evolution from simple to complex circuits. To handle the complexity, engineers over the years have developed some theorems to simplify circuit analysis. Such theorems include **Thevenin's and Norton's theorems**. Since these theorems are applicable to *linear* circuits, we first discuss the concept of circuit linearity. In addition to circuit theorems, we discuss the concepts of **superposition and maximum power transfer** in this chapter.

4.2 SUPERPOSITION

The idea of superposition rests on the linearity property.

The superposition principle states that the voltage across (or current through) an element in a linear circuit is the algebraic sum of the voltages across (or currents through) that element due to each independent source acting alone.

However, to apply the superposition principle, we must keep two things in mind:

- 1. We consider one independent source at a time while all other independent sources are turned off. This implies that we replace every voltage source by 0 V (or a short circuit), and every current source by 0 A (or an open circuit).
- 2. Dependent sources are left intact because they are controlled by circuit variables. With these in mind, we apply the superposition principle in three steps:

Steps to Apply Super position Principle:

- 1. Turn off all independent sources except one source. Find the output (voltage or current) due to that active source using nodal or mesh analysis.
- 2. Repeat step 1 for each of the other independent sources.
- 3. Find the total contribution by adding algebraically all the contributions due to the independent sources.

Analyzing a circuit using superposition has one major disadvantage: it may very likely involve more work. Keep in mind that superposition is based on linearity.

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Example 4.2: Use the superposition theorem to find v in the circuit in **Fig. 4.2**.

Solution:

Since there are two sources, let

$$\mathbf{v} = \mathbf{v_1} + \mathbf{v_2}$$

Where $\mathbf{v_1}$ and $\mathbf{v_2}$ are the contributions due to the 6-V voltage source and the 3-A current source, respectively. To obtain $\mathbf{v_1}$, we set the current source to zero, as shown in **Fig. 4.3(a)**. Applying **KVL** to the loop in **Fig. 4.3(a)** gives

$$12i_1 - 6 = 0 \Rightarrow i_1 = 0.5 \text{ A}$$

Thus,

$$v_1 = 4i_1 = 2 V$$

We may also use voltage division to get v_1 by writing

$$v_1 = \frac{4}{4+8}(6) = 2V$$

To get v_2 , we set the voltage source to zero, as in **Fig. 4.3(b)**. Using current division,

$$i_3 = \frac{8}{4+8}(3) = 2A$$

Hence,

$$v_2 = 4i_3 = 8 V$$

And we find

$$v = v_1 + v_2 = 2 + 8 = 10 \text{ V}$$

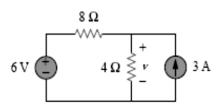


Figure 4.2 for Example 4.2.

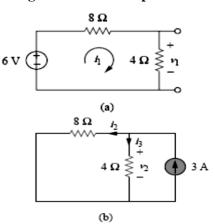
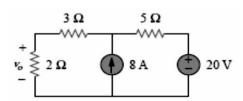


Figure 4.3 for Example 4.2: (a) Calculating v_1 , (b) calculating v_2 .

Practice problems:

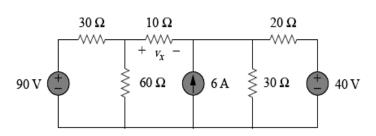
1-Using the superposition theorem, find $\mathbf{v_o}$ in the circuit in Figure below.

Answer: 12 V.



2- Use superposition to obtain v_x in the circuit of Figure below.

Answer: $v_x = -8.572V$.



Dept. of Computer Tech. Eng



4.3 THEVENIN'S THEOREM

It often occurs in practice that a particular element in a circuit is variable (usually called the load) while other elements are fixed. As a typical example, a household outlet terminal may be connected to different appliances constituting a variable load. Each time the variable element is changed, the entire circuit has to be analyzed all over again. To avoid this problem, Thevenin's theorem provides a technique by which the fixed part of the circuit is replaced by an equivalent circuit.

According to Thevenin's theorem, the linear circuit in **Fig. 4.8(a)** can be replaced by that in **Fig. 4.8(b)** is known as the Thevenin equivalent circuit; it was developed in 1883 by M. Leon Thevenin (1857–1926), a French telegraph engineer.

Thevenin's theorem states that a linear two-terminal circuit can be replaced by an equivalent circuit consisting of a voltage source $V_{\rm Th}$ in series with a resistor $R_{\rm Th}$, where $V_{\rm Th}$ is the open-circuit voltage at the terminals and $R_{\rm Th}$ is the input or equivalent resistance at the terminals when the independent sources are turned off.

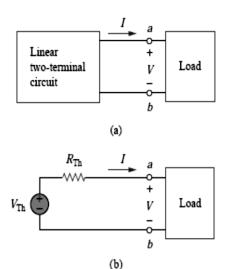


Figure 4.8 Replacing a linear two-terminal circuit by its Thevenin equivalent: (a) original circuit, (b) the Thevenin equivalent circuit.

To find the Thevenin equivalent voltage V_{Th} and resistance R_{Th} , suppose the two circuits in **Fig. 4.8** are equivalent. The open-circuit voltage across the terminals a-b in **Fig. 4.8(a)** must be equal to the voltage source V_{Th} in **Fig. 4.8(b)**, since the two circuits are equivalent. Thus V_{Th} is the open-circuit voltage across the terminals as shown in **Fig. 4.9(a)**; that is,

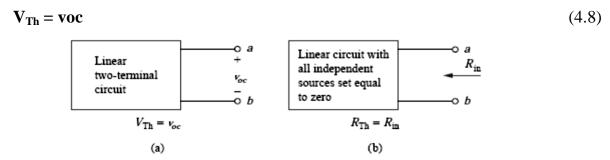


Figure 4.9 Finding V_{Th} and R_{Th} .

 \mathbf{R}_{Th} is the input resistance at the terminals when the independent sources are turned off, as shown in Fig. 4.9(b); that is,

$$\mathbf{R}_{\mathsf{Th}} = \mathbf{R}_{\mathsf{in}} \tag{4.9}$$



To apply this idea in finding the Thevenin resistance \mathbf{R}_{Th} , we need to consider two cases.

<u>CASE 1:</u> If the network has no dependent sources, we turn off all independent sources. \mathbf{R}_{Th} is the input resistance of the network looking between terminals \mathbf{a} and \mathbf{b} , as shown in **Fig. 4.9(b)**.

CASE 2: If the network has dependent sources, we turn off all independent sources. As with superposition, dependent sources are not to be turned off because they are controlled by circuit variables. We apply a voltage source $\mathbf{v_0}$ at terminals \mathbf{a} and \mathbf{b} and determine the resulting current $\mathbf{i_0}$. Then $\mathbf{R_{Th}} = \mathbf{v_0/i_0}$, as shown in **Fig. 4.10(a)**. Alternatively, we may insert a current source $\mathbf{i_0}$ at terminals $\mathbf{a-b}$ as shown in **Fig. 4.10(b)** and find the terminal voltage $\mathbf{v_0}$. Again $\mathbf{R_{Th}} = \mathbf{v_0/i_0}$. Either of the two approaches will give the same result. In either approach we may assume any value of $\mathbf{v_0}$ and $\mathbf{i_0}$. For example, we may use $\mathbf{v_0} = 1$ V or $\mathbf{i_0} = 1$ A, or even use unspecified values of $\mathbf{v_0}$ or $\mathbf{i_0}$.

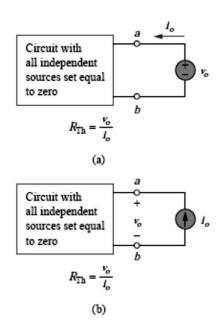


Figure 4.10 Finding RTh when circuit has dependent sources.

It often occurs that \mathbf{R}_{Th} takes a negative value. In this case, the negative resistance ($\mathbf{v} = -\mathbf{i}\mathbf{R}$) implies that the circuit is supplying power. This is possible in a circuit with dependent sources.

The current I_L through the load and the voltage V_L across the load are easily determined once the Thevenin equivalent of the circuit at the load's terminals is obtained, as shown in **Fig. 4.11(b)**. From **Fig. 4.11(b)**, we obtain

$$I_L = \frac{V_{Th}}{R_{Th} + R_L} \tag{4.10a}$$

$$V_L = R_L I_L = \frac{R_L}{R_{Th} + R_L} V_{Th} \tag{4.10b}$$

Note from Fig. 4.11(b) that the Thevenin equivalent is a simple voltage divider, yielding V_L by mere inspection.

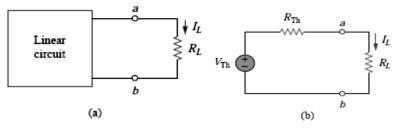


Figure 4.11 A circuit with a load :(a) original

circuit, (b) Thevenin equivalent.



Example 4.4: Find the Thevenin equivalent circuit of the circuit shown in Fig. 4.12, to the left of the terminals a-b. Then find the current through $R_L = 6$, 16, and 36 Ω .

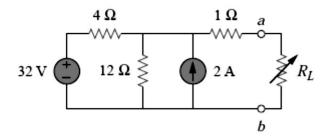


Figure 4.12 For Example 4.4.

Solution:

We find R_{Th} by turning off the 32-V voltage source (replacing it with a short circuit) and the 2-A current source (replacing it with an open circuit). The circuit becomes what is shown in **Fig. 4.13(a)**. Thus,

Figure 4.13 For Example 4.4: (a) finding R_{Th} , (b) finding V_{Th} .

To find V_{Th} , consider the circuit in **Fig. 4.13(b)**. Applying mesh analysis to the two loops, we obtain

$$-32 + 4i_1 + 12(i_1 - i_2) = 0$$
, $i_2 = -2$ A

Solving for i_1 , we get $i_1 = 0.5$ **A**. Thus,

$$V_{Th} = 12(i1 - i2) = 12(0.5 + 2.0) = 30 \text{ V}$$

The Thevenin equivalent circuit is shown in Fig. 4.14. The current through $R_{\rm L}$ is

$$I_L = \frac{V_{Th}}{R_{Th} + R_L} = \frac{30}{4 + R_L}$$

When $\mathbf{R}_L = 6$, $I_L = \frac{30}{10} = 3 A$
When $R_L = 16$, $I_L = \frac{30}{20} = 1.5 A$
When $RL = 36$, $I_L = \frac{30}{40} = 0.75 A$

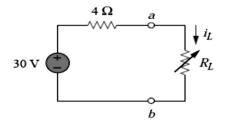
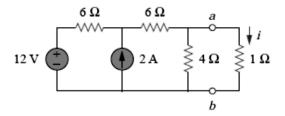


Figure 4.14 The Thevenin equivalent circuit



<u>Practice problem:</u> Using Thevenin's theorem, find the equivalent circuit to the left of the terminals in the circuit in Figure below. Then find i.

Answer: $V_{Th} = 6 \text{ V}, R_{Th} = 3 \Omega, i = 1.5 \text{ A}.$



4.4 NORTON'S THEOREM

In 1926, about 43 years after Thevenin published his theorem, E. L. Norton, an American engineer at Bell Telephone Laboratories, proposed a similar theorem.

Norton's theorem states that a linear two-terminal circuit can be replaced by an equivalent circuit consisting of a current source I_N in parallel with a resistor R_N , where I_N is the short-circuit current through the terminals and R_N is the input or equivalent resistance at the terminals when the independent sources are turned off.

Thus, the circuit in Fig. 4.15(a) can be replaced by the one in Fig. 4.15(b).

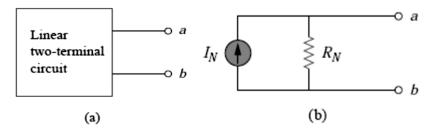


Figure 4.15 (a) Original circuit, (b) Norton equivalent circuit.

We are mainly concerned with how to get \mathbf{R}_N and \mathbf{I}_N . We find \mathbf{R}_N in the same way we find \mathbf{R}_{Th} . In fact, the Thevenin and Norton resistances are equal; that is,

$$\mathbf{R_N} = \mathbf{R_{Th}} \tag{4.11}$$

To find the Norton current I_N , we determine the short-circuit current flowing from terminal a to b in both circuits in **Fig. 4.15**. It is evident that the short-circuit current in **Fig. 4.15(b)** is I_N . This must be the same short-circuit current from terminal a to b in **Fig. 4.15(a)**, since the two circuits are equivalent. Thus,

$$\mathbf{I}_{\mathbf{N}} = \mathbf{i}_{\mathbf{sc}} \tag{4.12}$$

Dependent and independent sources are treated the same way as in Thevenin's theorem. Observe the close relationship between Norton's and Thevenin's theorems: $\mathbf{R}_N = \mathbf{R}_{Th}$ as in Eq. (4.11), and

$$\mathbf{I_N} = \frac{v_{Th}}{R_{Th}} \tag{4.13}$$



This is essentially source transformation. For this reason, source transformation is often called Thevenin-Norton transformation.

We can calculate any two of the three using the method that takes the least effort and use them to get the third using Ohm's law. **Example 4.10** will illustrate this. Also, since

$$\mathbf{V}_{\mathsf{Th}} = \mathbf{v}_{\mathsf{oc}} \tag{4.14a}$$

$$\mathbf{I_N} = \mathbf{i_{sc}} \tag{4.14b}$$

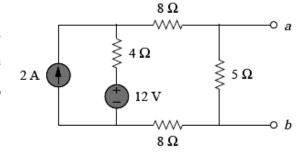
$$R_{Th} = \frac{voc}{isc} = R_N \tag{4.14c}$$

the open-circuit and short-circuit tests are sufficient to find any Thevenin or Norton equivalent.

Example 4.5 Find the Norton equivalent circuit of the circuit in **Fig. 4.16**.

Solution:

We find \mathbf{R}_N in the same way we find \mathbf{R}_{Th} in the Thevenin equivalent circuit. Set the independent sources equal to zero. This leads to the circuit in **Fig. 4.17(a)**, from which we find \mathbf{R}_N . Thus,



$$R_N = 5||(8 + 4 + 8)| = 5||20| = \frac{20 \times 5}{25} = 4 \Omega$$

Figure 4.16 For Example 4.5.

To find I_N , we short-circuit terminals a and b, as shown in **Fig. 4.17(b)**. We ignore the 5- Ω resistor because it has been short-circuited. Applying mesh analysis, we obtain

$$i_1 = 2 A$$
, $20i_2 - 4i_1 - 12 = 0$

From these equations, we obtain

$$\mathbf{i}_2 = \mathbf{1} \mathbf{A} = \mathbf{i}_{sc} = \mathbf{I}_N$$

Alternatively, we may determine I_N from V_{Th}/R_{Th} . We obtain V_{Th} as the open-circuit voltage across terminals a and b in Fig. 4.17(c). Using mesh analysis, we obtain

$$i_3 = 2 A$$

$$25i_4 - 4i_3 - 12 = 0 \implies i_4 = 0.8 \text{ A}$$

and

$$v_{oc} = V_{Th} = 5i_4 = 4 \text{ V}$$



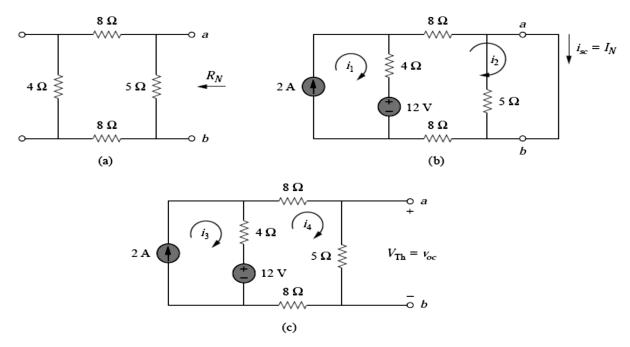


Figure 4.17 For Example 4.5; finding: (a) RN, (b) IN = isc, (c) V_{Th} = voc.

Hence.

$$I_N = \frac{VTh}{RTh} = \frac{4}{4} = 1 A$$

as obtained previously. This also serves to confirm Eq. that $\mathbf{R}_{Th} = \mathbf{v}_{oc}/\mathbf{i}_{sc} = 4/1 = 4\ \Omega$. Thus, the Norton equivalent circuit is as shown in Fig. 4.18.

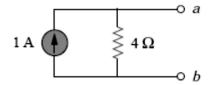
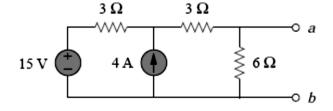


Figure 4.18 Norton equivalent of the circuit in Fig. 4.16.

Practice problem: Find the Norton equivalent circuit for the circuit in Figure below.

Answer: $R_N = 3 \Omega$, $I_N = 4.5 A$.



4.5 MAXIMUM POWER TRANSFER

In many practical situations, a circuit is designed to provide power to a load. While for electric utilities, minimizing power losses in the process of transmission and distribution is critical for efficiency and economic reasons, there are other applications in areas such as communications where it is desirable to maximize the power delivered to a load. We now address the problem of delivering the maximum power to a load when given a system with known internal losses.

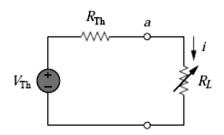


Figure 4.19 The circuit used for maximum power transfer.



The Thevenin equivalent is useful in finding the maximum power a linear circuit can deliver to a load. We assume that we can adjust the load resistance R_L . If the entire circuit is replaced by its Thevenin equivalent except for the load, as shown in **Fig. 4.19**, the power delivered to the load is

$$p = i^2 R_L = \left(\frac{V_{Th}}{R_{Th} + R_L}\right)^2 R_L \tag{4.15}$$

For a given circuit, V_{Th} and R_{Th} are fixed. By varying the load resistance R_L , the power delivered to the load varies as sketched in Fig. 4.20. We notice from Fig. 4.20 that the power is small for small or large values of R_L but maximum for some value of R_L between 0 and ∞ . We now want to show that this maximum power occurs when R_L is equal to R_{Th} . This

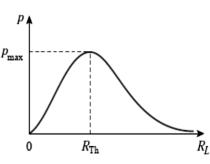


Figure 4.20 Power delivered to the load as a function of RL

Maximum power is transferred to the load when the load resistance

equals the Thevenin resistance as seen from the load $(R_L = R_{Th})$.

To prove the maximum power transfer theorem, we differentiate p in Eq. (4.15) with respect to \mathbf{R}_{L} and set the result equal to zero. We obtain

is known as the maximum power theorem.

$$\frac{dp}{dR_L} = V_{Th}^2 \left[\frac{(RTh + RL)^2 - 2R_L(R_{Th} + R_L)}{(R_{Th} + R_L)^4} \right] = V_{Th}^2 \left[\frac{(R_{Th} + R_L - 2R_L)}{(R_{Th} + R_L)^3} \right] = \mathbf{0}$$

This implies that

$$0 = (R_{Th} + R_L - 2R_L) = (R_{Th} - R_L)$$
(4.16)

which yields

$$\mathbf{R}_{L} = \mathbf{R}_{Th} \tag{4.17}$$

showing that the maximum power transfer takes place when the load resistance \mathbf{R}_L equals the Thevenin resistance \mathbf{R}_{Th} . We can readily confirm that $\mathbf{Eq.}$ (4.17) gives the maximum power by showing that $\mathbf{d}^2\mathbf{p}/\mathbf{dR}^2_L < 0$.

The maximum power transferred is obtained by substituting Eq. (4.17) into Eq. (4.15), for

$$p_{max} = \frac{V_{Th}^2}{4R_{Th}} \tag{4.18}$$

Equation (4.18) applies only when $R_L = R_{Th}$. When $R_L \neq R_{Th}$, we compute the power delivered to the load using Eq. (4.15).

Example 4.6: Find the value of R_L for maximum power transfer in the circuit of Fig. 4.21. Find the maximum power.



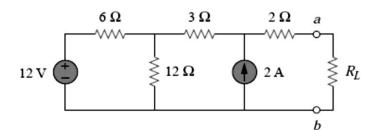


Figure 4.21 For Example 4.6.

Solution:

We need to find the Thevenin resistance R_{Th} and the Thevenin voltage V_{Th} across the terminals a-b. To get R_{Th} , we use the circuit in Fig. 4.22(a) and obtain

Figure 4.22 For Example 4.6: (a) finding R_{Th} , (b) finding V_{Th} .

To get V_{Th} , we consider the circuit in Fig. 4.22(b). Applying mesh analysis,

$$-12 + 18i_1 - 12i_2 = 0$$
, $i_2 = -2$ A

Solving for i_1 , we get $i_1 = -2/3$. Applying KVL around the outer loop to get V_{Th} across terminals a-b, we obtain

$$-12 + 6i_1 + 3i_2 + 2(0) + V_{Th} = 0 \Rightarrow V_{Th} = 22 \text{ V}$$

For maximum power transfer,

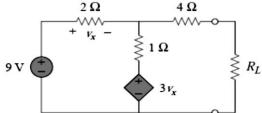
$$R_L = R_{Th} = 9 \Omega$$

and the maximum power is

$$p_{max} = \frac{V_{Th}^2}{4RL} = \frac{22^2}{4 \times 9} = 13.44 W$$

Practice problem: Determine the value of R_L that will draw the maximum power from the rest of the circuit in Figure below. Calculate the maximum power.

Answer: 4.22 Ω, 2.901 W.

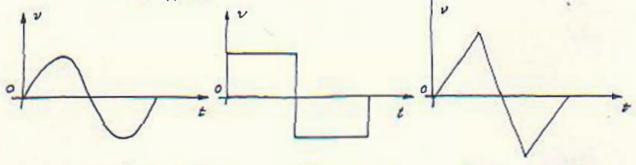


5. Sinusaidal Alternating Waveforms

EE6

6.1 Some Representative Types of AC Waveforms

ac voltage or ac current refers to alternating voltage or current. The term alternating indicates only that waveforms alternate between two prescribed levels in a set time sequence. To be absolutely correct the term sinusoidal, square, trainquiar must be also applied.



Sinusoidal Wantform

Squar Wane

Triangular Wane .

The above types of wereforms (or signals), are often encountered in electrical engineering applications, but they are not the only one. There are too many different types of wonsforms that have different shapes matching certain applications.

5.3 Definitions Related to AC Waveforms

PES

+ The Cycle

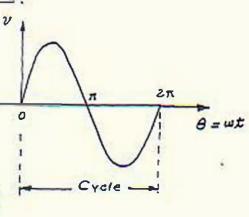
: One complete set of

positive and negative values of an

alternating quantity is called a

(CYCle). Complete cycle is said to

spread over 360° or 2x radians



. 360° = 2 x radians

$$\Rightarrow 1 \text{ rad.} = \frac{360}{2\pi} = \frac{180}{\pi}$$

- To convert from degrees to radians

Radians = (
$$\frac{\pi}{180}$$
) x degrees

- To convert from radions to degrees

Degrees =
$$(\frac{180}{\pi}) \times radians$$

For examples

$$90^{\circ} \rightarrow rad. = \frac{\pi}{180} \times 90^{\circ} = \frac{\pi}{2}$$

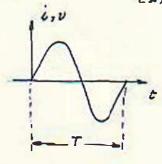
$$30^{\circ} \rightarrow rad. = \frac{\pi}{180} \times 30 = \frac{\pi}{6}$$

$$\frac{\pi}{3} \rightarrow \text{deg.} = \frac{180}{\pi} \times \frac{\pi}{3} = 60$$

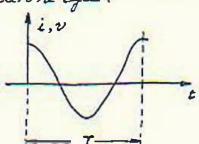
$$\frac{3\pi}{2} \Rightarrow \deg \cdot = \frac{/80}{\pi} \times \frac{3\pi}{2} = 270^{\circ}$$

* Time Period (T)

: It is the time taken by an alternating quantity to complete. one cycle.



$$T = \frac{1}{f}$$



Instantaneous Value

EE6

The magnitude of a waveform (alternating voltage or current) at any instant of time. It is denoted by small letters such as $v, e, \cdot e_z \cdot i$, i_2 , p ... etc.

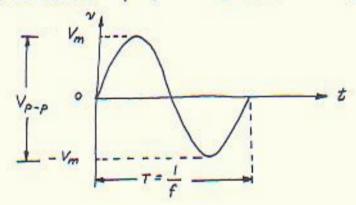
* Amplitude (or Peak Value or Maximum Value)

-: The maximum value

(positive or negative) of on alternating quantity is called amplitude. It is denoted by capital letters, such as Em, Vm, Im, etc.

* Frequency

-: It is the number of cycles that occur in one second



+ Peak to Peak Value

: It is denoted by Ep-p or Vp-p and represents the full voltage between positive and negative peaks of the waveform (see the figure above.).

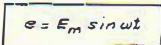
In general form for the sinuspidal voltage or current

e = Em sin 0	& in deg.
e = E _m sin wt	8 = wt
e = Em sin 2πft	$\omega = \epsilon \pi f$
$e = E_m \sin \frac{2\pi}{T} t$	$f = \frac{1}{T}$

Phase Relations 6.4

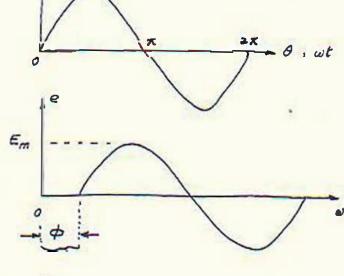
EE6

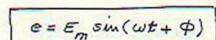
A sinusoidal waveform may start from zero as had been shown emplier, or it many be shifted to the left or to the right as shown.

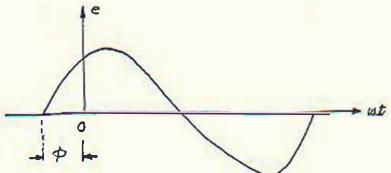


* it has zero phase shift.

$$e = E_m \sin(\omega t - \phi)$$



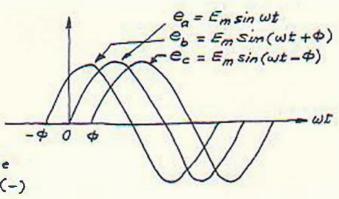




In the figure shown, three sinusoidal waveform are plotted with different phases:

$$e_b = E_m \sin(\omega t + \Phi)$$

 $e_c = E_m \sin(\omega t - \Phi)$



Some Useful Relations

$$sin(\omega t + 90) = sin(\omega t + \frac{\pi}{2}) = aos(\omega t)$$

 $sin(\omega t - 90) = cos(\omega t - \frac{\pi}{2}) = sin \omega t$
 $sin(-\theta) = -sin\theta$
 $cos(-\theta) = cos\theta$
 $-sin(\theta) = sin(\theta \pm 180^{\circ})$
 $-cos(\theta) = cos(\theta \pm 180^{\circ})$

Examples

EE6

(a).
$$v = 10 \sin(\omega t + 30)$$

 $i = 5 \sin(\omega t + 70^{\circ})$

(b).
$$v = 10 \sin(\omega t - 20^{\circ})$$

 $i = 15 \sin(\omega t + 60^{\circ})$

(c).
$$i = 2\cos(\omega t + 10^{\circ})$$

 $v = 3\sin(\omega t - 10^{\circ})$

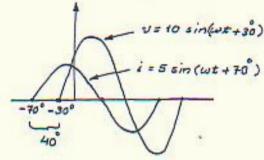
(d).
$$\dot{L} = -2\cos(\omega t - 60^{\circ})$$

 $v = 3\sin(\omega t - 150^{\circ})$

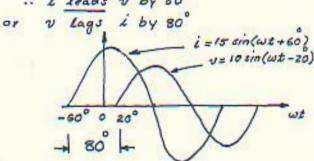
(e).
$$i = -\sin(\omega t + 30^{\circ})$$

 $v = 2\sin(\omega t + 10^{\circ})$

Solutions (a).



The phase difference = 40° i leads v by 40° or v lags i by 40°



Similarly you can find the results of c, d and e , and the results are:

(c).

$$i = 2\cos(\omega t + i0) = 2\sin(\omega t + i0 + 90) = 2\sin(\omega t + i00)$$

:. $i = 2\sin(\omega t + i00)$?
 $v = 3\sin(\omega t - i00)$ \Rightarrow The phase difference = $(100 + i0) = 110$

:. i lead v by 110

or
$$i = -5in(\omega t + 30) = 5in(\omega t + 30 + 180) = 5in(\omega t + 210)$$

 $i = 5in(\omega t + 210)$ }
 $v = 25in(\omega t + 100)$ } The phase difference is 200

:. i lead v by 200

5.5 Average Value

EEG

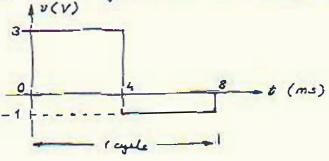
The average value of on alternating waveform is the equivalent (dc) value over a complete cycle. In general the average value of a waveform is given as

For complex woniforms the area under the curve is difficult to obtain directly, so it can be evaluated by integration over the specified period of time.

Example

Find the average value of the waveform shown over one full

yelc.



Solution

$$V_{av.} = \frac{Aera under the curve}{Length of the curve}$$

$$= \frac{(3)(4) - (1)(4)}{8} = 1 \text{ Volt}$$

Example

: Calculate the average value of the waveform shown.

Solution

= = ginen z= V sin 8

$$V_{AV} = \frac{2V_m}{\pi}$$

$$V_{OU} = 0.637 V_m$$

D X Sin B

 $\sum_{\alpha} V_{m} \sin \theta d\theta = \begin{vmatrix} -\cos \theta \\ \cos \pi - \cos \theta \end{vmatrix}^{R}$ $= V_{m} (\cos \pi - \cos \theta)$ $= 2V_{m}$

Note; This value of Vau is for one half cycle only!

For complete cycle (one cycle) => Vau = 0

5.6 Effective Value (or Root Mean Square Value rms)

The effective value

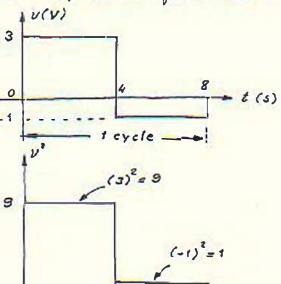
(or the rms value) of an alternating waveform is given by the steady (dc) current which when flowing through a go ineaircuit, for a given time produces the same heat produced by the alternating current when fleeing through the same circuit for the same time.

$$I_{rms} = I_{eff} = \frac{\sqrt{ji(t)}}{T} dt$$

For simple shaped waveforms

$$I_{rms} = I_{eff} = \sqrt{\frac{A_{rea}[i(t)^2]}{T}}$$

Example Find the effective or rms value of the warmform shown.



Solution

$$V_{eff} = \sqrt{\frac{R_{16a} \left[v^{2}(t)\right]}{T}}$$

$$= \sqrt{\frac{(9)(4) + (1)(4)}{8}}$$

$$= 2.236 \text{ V}$$

Example

For the waneform given by (i = Imsinut), calculate the rms value.

$$\frac{\text{Solution}}{\text{Solution}}: \qquad I_{eff} = I_{rms} = \int_{0}^{2\pi} \frac{i^{2}}{2\pi} d\theta$$

$$I_{rms} = \sqrt{\frac{1}{2\pi}} \int_{0}^{2\pi} (I_{m} \sin \theta)^{2} d\theta = \sqrt{\frac{I_{m}}{2\pi}} \int_{0}^{2\pi} \sin^{2}\theta d\theta$$

$$= \sqrt{\frac{I_{m}}{2\pi}} \int_{0}^{2\pi} \frac{(1 - \cos 2\theta)}{2} d\theta = \sqrt{\frac{I_{m}}{4\pi}} \left| \theta - \frac{\sin 2\theta}{2} \right|_{0}^{2\pi}$$

$$= \sqrt{\frac{I_{m}}{4\pi}} \times 2\pi = \sqrt{\frac{I_{m}}{2}} = \frac{1}{\sqrt{2}} I_{m}$$

$$I_{rms} = \frac{1}{\sqrt{2}} I_m = 0.707 I_m$$

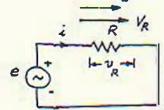
sinuspidal wantorms (voltage or current)

5.7 Circuit Elements in the Phasor Domain

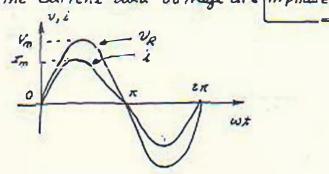
@ AC Through Pure Ohmic Resistor Alone

In the cct. shown, let the applied Voltage be given by:

E = Em sin wt = Esin 0

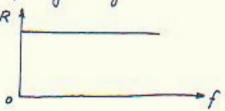


@ In resistors, the current and voltage are inphase



EE6

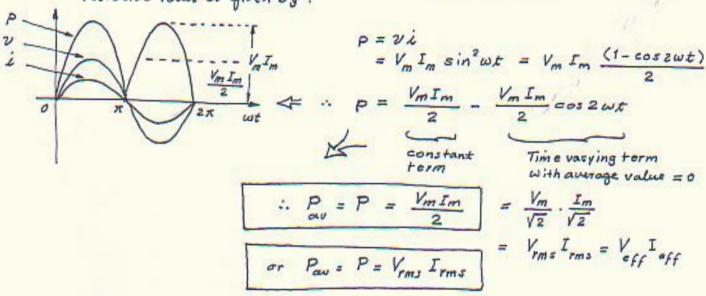
+ The frequency response of the resistive load is uniform, ie, the value of the resistance doesn't change as the frequency changes.



The Average Power (Real Power)

-: The instantaneous power to the

resistive load is given by :



* The Power factor is defined as the cosine of the phase angle between the voltage and current, ie,

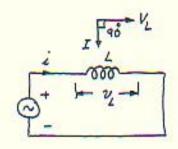
where ϕ is the phase difference angle between i and v. Since the voltage and current are in phase (ie, the phase difference = 0), then; $\phi = 0$

AC Through Pure Inductance Alone

EE6

$$v_{L} = L \frac{di}{dt}$$

If the current i is given by:



$$\Rightarrow v_L = L \cdot \frac{d}{dt} (I_m sin \omega t) = \omega L I_m cos \omega t$$

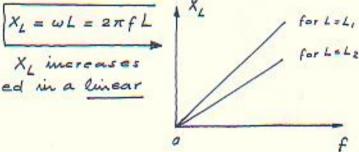
From the equations of i and v_L , it is clear that v_L lead i by an angle of (98), or the current i lags v_L by (90°).

- Let us define the reactonce of an inductor, in a way similar to Ohm's law:

:. Reactance =
$$X_L = \frac{V_m}{I_m} = \frac{\omega L I_m}{I_m} = \omega L$$
of an inductor

* The frequency response of the pure inductor is derived from the relation:

and is shown in the figure. X1 increases as the frequency is increased in a linear relationship.



Power factor

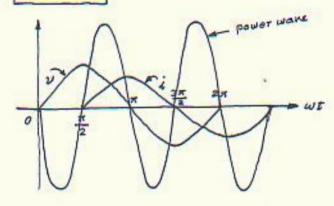
The power factor p.f = cos
$$\phi$$

since $\phi = 90^{\circ}$
 \Rightarrow :. p.f = cos $90^{\circ} = 0$

The average power

The instantoneous power for the pune inductive circuit is:

EE6



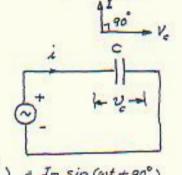
AC Through a Pure Capacitar

: For the capacitor of the figure shown:

let vo = Vm sinut

$$\Rightarrow i_e = \omega C V_m \cos \omega t$$

$$= \omega C V_m \sin(\omega t + 90) = I_m \sin(\omega t + 90^\circ)$$



It is clear, from the equations of ic and vc , that ic leads vc by on angle of 90° or vc lags ic by 90°

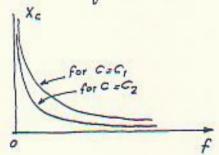
* The capacitive reactonce (Xc) is:

$$X_{c} = \frac{V_{m}}{I_{m}} = \frac{V_{m}}{\omega c V_{m}} = \frac{1}{\omega C}$$

$$\therefore X_C = \frac{1}{\omega C}$$

The frequency response of a pure capacitor is derived from the relation above and is shown in the figure.

is increased in a non-linear behaviour.



* Power factor

EE6

- Since the phase difference between ic and Ve is 90, this means that $\phi = 90^\circ$, then:

* The average power

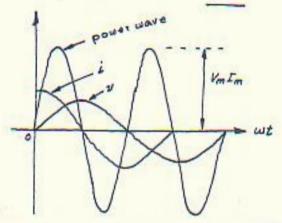
The instantaneous power p is given as:

$$P = V_{e}i_{e} = V_{m}I_{m} \sin \omega t. \sin (\omega t + 90)$$

$$\Rightarrow P = \frac{V_{m}I_{m}}{2} \sin 2\omega t$$

This quantity has an average

:. The average power of a pure capacitive load is zero.



Summary of AC paramters for R, L&C

Element Paramter	R	L	C
Power factor	1	0	0
Average Power Pav = P	Vm Im = Vrms Irms	0	0
Impedance Z	R	XL = WL	$X_c = \frac{1}{\omega C}$
Phase difference p between V& i	0	90° V leads i	90 or ic leads Vo
Frequency Response	Uniform (constant)	Linear (Increasing)	Non-linear (Decreasing)

5.8 Calculating Alternating Quantities

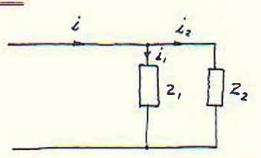
EE6

Example

calculate i, if i, and iz are:

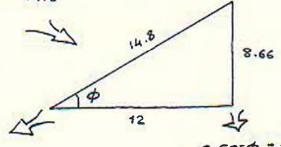
$$\lambda_1 = 7 \sin \omega t$$

$$\lambda_2 = 10 \sin(\omega t + 60)$$



Solution (time domain calculation)

Divide and multiply be $\sqrt{12^2 + 8.66^2} = 14.8$, we get



$$i = 14.8 (\cos \phi \sin \omega t + \sin \phi \cos \omega t)$$

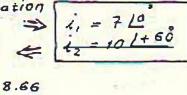
$$\sin \phi = \frac{8.66}{14.8}$$

$$tan \phi = \frac{8.66}{12}$$

$$ar \phi = tan^{-1} \left(\frac{8.66}{12} \right)$$

Solution (phasor domain colculation)

In phasor representation



\$=60°_

phasor diagram

= Horizontal components = 7 cos 0° + 10 cos 60° = 12

= 7 sin 0 + 10 sin 60

. Rosultant = V122+ 8.66 = 14.8 A

The phase angle
$$\phi = \tan \frac{1}{12} \frac{8.66}{12} = 35.8^{\circ} \implies i = 14.8 \frac{35.8^{\circ}}{35.8^{\circ}}$$

$$\therefore i = 14.8 \sin(\omega t + 35.8^{\circ}) \quad \text{as before.}$$

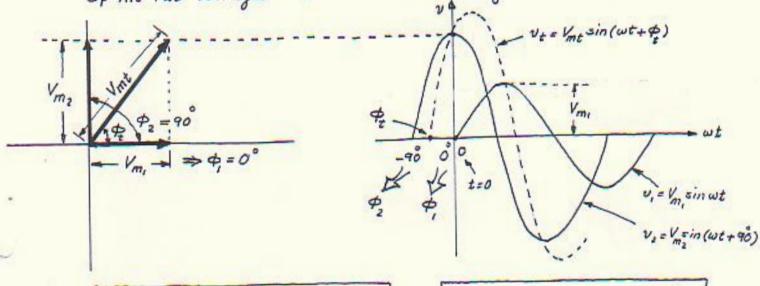
Phasors

-: The phasor is a radius vector that has a constant amplitude at a fixed angle from the positive real axis and that represents a sinusoidal voltage or current in the vector domain, at t = 0.

For example, if we have to voltages v, and v2 given by:

$$v_1 = V_{m_1} \sin \omega t$$
and
$$v_2 = V_{m_2} \sin (\omega t + 90^\circ)$$

We will show the time domain and phasor domain representation of the two voltages and the resultant voltage.



Phasor domain representation

 $v_{i} = V_{m_{i}} / 0$ $v_{z} = V_{m_{z}} / 90^{\circ}$ $v_{t} = V_{m_{i}} / 0^{\circ} + V_{m_{i}} / 90^{\circ}$

Time domain representation

v, = Vm, sin wt

 $v_2 = V_{m_2} \sin(\omega t + 90)$

 $v_t = V_{mt} \sin(\omega t + \theta_T)$

NOTE + In general, it is customary to use the rms (or effective) values (instead of max. values) in the phasor representation, as will be shown in the following examples.

EE6

- Convert the following quantities from the time domain to the phasor domain .

- a. V2 50 sinut
- b. 69.6 sin(wt + 72)
- c. 45 sin wt
- d. 80 cos wt

Solution

Time domain		Phaser demain	
a.	V2 (so) sin wt	50 LO° 49.21/12°	
ь.	69.6 sin (wt +72) 45 sin wt	31.82 LO°	
c.	80 cos wt	31.82 190	

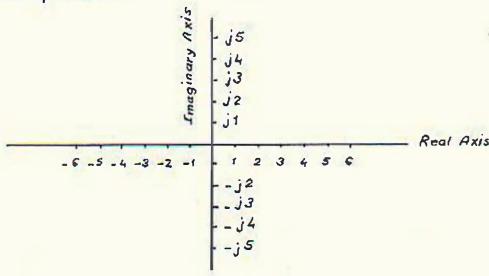
Example

-: Write the sinusoidal expression for the following phasors if the frequency is 60 Hz.

Solution

Phasor domain	Time domain	
a. I = 10 130°	$i = \sqrt{2} (10) \sin (\omega t + 30^{\circ})$ = $14.14 \sin (2\pi f t + 30^{\circ})$: $i = 14.14 \sin (377t + 30^{\circ})$	
b. V = 115 /-70°	$v = \sqrt{2} (115) \sin(2\pi f t - 70^{\circ})$.: $v = 162.6 \sin(377t - 70^{\circ})$	

A complex number is a number that represents a point in a two dimensional plane located with reference to two distinct axes. It defines a vector drawn from the origin to that point. The plane used to represent complex numbers is called the complex plane; the two axes are called the real and imaginary. It is important that the scale on the axis of imaginaries must be the same as that on the axis of reals.



The complex plane

complex numbers can be represented in the following forms:

Rectangular form: It is customary in this form to denoted the complex numbers as:

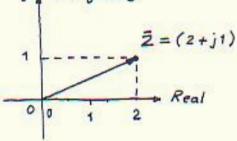
where; Z is the complex number.

R is the real part.

X is the imaginary part.

j is an operator = V-1 and is equivalent to 90 phase engle.

For example, the complex number Z = 2+j1 is represented [EE6] in the complex plane as shown: j imaginary



Mathematical
Operations in the Rectangular Form

Equality

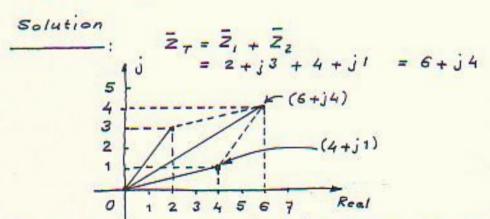
If we have two complex numbers
$$Z = x + jy$$
 and $W = u + jv$, then if $Z = W$, then it follows that $x = u$ and $v = y$

Example: Given that
$$\bar{Z}_1 = 5 + j10$$
, and $Z_2 = 5 + jX$. If $\bar{Z}_1 = \bar{Z}_2$, find the value of X

* Addition and Subtraction

that a real number equal to the sum of the real components and an imaginary number equal to the sum of the imaginary components.

Example: Given
$$\overline{Z}_1 = (2+j3)$$
, and $\overline{Z}_2 = (4+j1)$. Find \overline{Z}_7 , if $\overline{Z}_7 = \overline{Z}_1 + \overline{Z}_2$.



The parallelogram method is used for adding and subtraction of quantities in the complex plane.

Multiplication

- The product of a real and imaginary number is imaginary; Thus:

- The product of two positive imaginary numbers is real and negative, thus:

$$(j2)(j3) = -6$$

and
$$(j3)(-j4) = +12$$

Complex numbers are multiplied by the ordinary rules of algebra. As an example;

$$(2+j3)(4+j1) = (2)(4) + (2)(j1) + (j3)(4) + (j3)(j1)$$

= 8 + j2 + j12 - 3 = 5+j4

$$(a+jb)(c+jd) = (ac-bd) + j(ad+bc)$$

- By way of illustration, let us comider the division of $\vec{V} = (S+j10)$ by $\vec{I} = (2+j1)$.

+ The first step

is to multiply both numerator and denominator by (2-j1) which is called complex conjugate of the denominator and usually denoted by asterisk,

$$\vec{I} = 2 + j1$$

$$\vec{I}^* = 2 - j1 \iff complex conjugate of \vec{I}.$$

$$(\vec{I})(\vec{I}^*) = Real \ value$$

then; for
$$\frac{5+j10}{2+j1} = \frac{(5+j10)(2-j1)}{(2+j1)(2-j1)} = \frac{20+j15}{5}$$

$$\frac{5+j10}{2+j1} = 4+j3$$

$$\frac{\text{In general}}{c+jd} : \frac{a+jb}{c+jd} = \frac{ac+bd}{c^2+d^2} + j \frac{bc-ad}{c^2+d^2}$$

* The j operator

EEG

$$j^{2} = -1$$

$$j^{2} = -1$$

$$j^{3} = j^{2}$$

$$j^{4} = j^{2}$$

$$j^{2} = +1$$

$$\Rightarrow 270^{\circ} \text{ ccw rotation}$$

$$\Rightarrow 270^{\circ} \text{ ccw rotation}$$

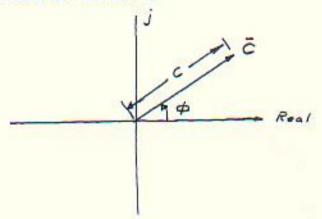
and we have also:

$$\frac{!}{j} = -j$$

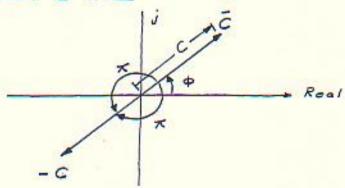
Polar Form

In this form , the complex number is represented as

where C is the magnitude only, and \$\phi\$ is always measured counter-clockwise



* A negative sign has the effect shown in the figure



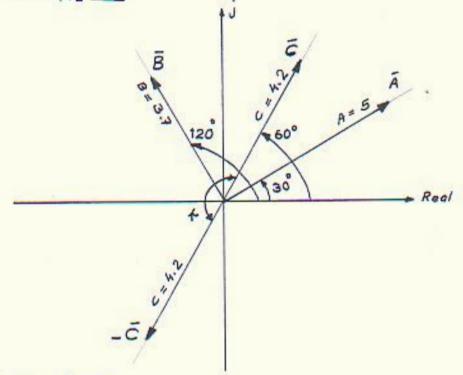


: Sketch the following complex numbers in the complex plane.

a.
$$\vec{A} = 5/30^{\circ}$$

 $\vec{B} = 3.7/120^{\circ}$
 $\vec{C} = -4.2/60^{\circ}$

Solution

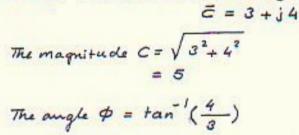


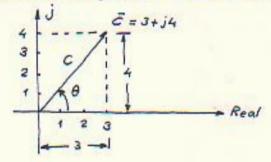
Conversion Between Forms

Rectangular to Polar

Example

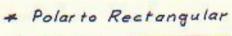
Convert the following from rectangular to polar form.





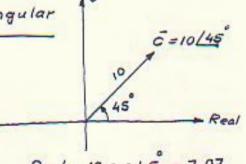
. C in the polar form is C = 5 /53.13

= 53.13°



Example

from polar to rectangular form :



Real = 10 cos 45 = 7.07 Imaginary = 10 sin 45 = 7.07

AC Network Analysis

EE8

Demonstrative Examples

* Mesh (Loop) Analysis

Example

in the circuit shown; prove that this power equals the power in the circuit resistors. Use mesh analysis in your solution.

Solution

$$100 \underline{10}^{\circ} = \bar{I}_{1}(8-j6) - \bar{I}_{2}(-j6)$$

$$\Rightarrow 100 \underline{10}^{\circ} = \bar{I}_{1}(8-j6) + \bar{I}_{2}(j6) - 0$$

$$+ 400 p 2$$

$$0 = \bar{I}_{2}(3+j4-j6) - \bar{I}_{1}(-j6)$$

$$= \bar{I}_{1}(j6) + \bar{I}_{2}(3-j2) - 2$$

$$\bar{I}_1 = \frac{\Delta_1}{\Delta}$$
 and $\bar{I}_2 = \frac{\Delta_2}{\Delta}$

$$\Delta = \begin{vmatrix} (8-j6) & j6 \\ j6 & (3-j2) \end{vmatrix} = (8-j6)(2-j2) - (j6)^{2}$$

$$\Rightarrow \Delta = 58.703 \left[-35.33 \right]$$

$$\Delta_{i} = \begin{vmatrix} 10010^{\circ} & j6 \\ 0 & (3-j2) \end{vmatrix} = (300-j2) = 3601 - 33.69$$

and
$$\Delta z = \begin{vmatrix} (8-i6) & 100 \\ 0 & 0 \end{vmatrix} = i600 = 600 \frac{190}{}^{\circ}$$

$$\vec{I}_{2} = \frac{\Delta_{1}}{\Delta} = \frac{360 \left[-33.69^{\circ}\right]}{58.703 \left[-35.23^{\circ}\right]} = 6.133 \left[1.64^{\circ}\right]$$

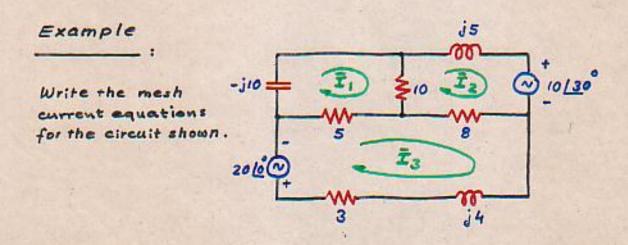
$$\vec{I}_{2} = \frac{\Delta_{2}}{\Delta} = \frac{600 \left[90^{\circ}\right]}{58.703 \left[-36.33^{\circ}\right]} = 10.22 \left[125.33^{\circ}\right]$$

:. Total Power output = total power absorbed by resistors

$$\therefore VI_1 = I_1^2(8) + I_2^2(3)$$

$$(100)(6.133) = 300.4 + 313.3$$

$$\therefore 613.3 W = 613.7 W$$



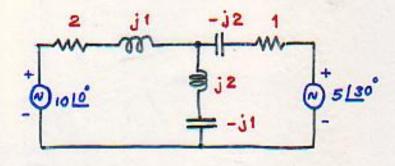
___: The three mesh current equations are

+ Loop 3

Then you can find
$$\bar{I}_1 = \frac{\Delta_1}{\Delta}$$
 , $\bar{I}_2 = \frac{\Delta_2}{\Delta}$ and $\bar{I}_3 = \frac{\Delta_3}{\Delta}$

Homework

For the ect. shown, determine the branch voltages and currents and the power delivered by the source using mech analysis.



All resistance and reactance values are in Ohms .

* Active, Reactive and Apparent Power (The Power Triangle)

EET

* The Apparent Power

: It is the product of the rms values of the applied voltage and the circuit current

⇒ Apparent power = S = VI = (IZ). I = I²Z ومذت بشاس volt - ampera (VA)

* The Active Power

.: It is the power which is actually dissipated in the circuit resistance.

> Active power = P = I2R = VI cos p watts (W)

* The Reactive Power

: It is the power developed in the inductive reactance of the circuit.

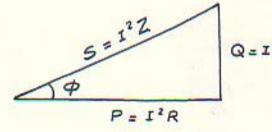
> Reactive Power = Q = I2X1 = I2(Zsin +)

: Q = VI sin b

و مدامت بسیاس

Volf-amper reactive (VAR)

These three powers are shown in the power triangle: From the power triangle:



The power triangle

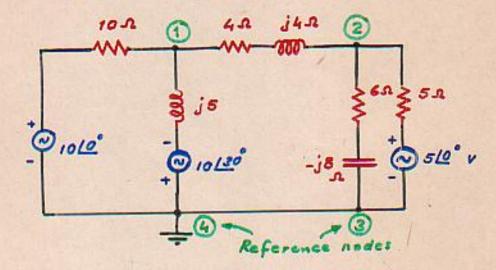
* The quality factor of the Coil

-: It is defined as the reciprocal of the power factor of the coil . Hence:

 $Q factor = \frac{1}{power factor} = \frac{1}{\cos \phi} = \frac{Z}{R_c} \frac{\sqrt{R_{c}^2 \chi_L^2}}{R_c}$ if Re is too small compared with X1 , then: coil

* Nodal Analysis

Example : Write the nodal equations for the circuit shown;



Solution

two equations have to be written:

Node 1

$$\bar{V}_{1}\left(\frac{1}{10} + \frac{1}{(4+j4)} + \frac{1}{j5}\right) - \bar{V}_{2}\left(\frac{1}{4+j4}\right) = \frac{1000}{10} - \frac{10000}{j5}$$

Node 2

$$\bar{V}_2(\frac{1}{4+j4}+\frac{1}{5}+\frac{1}{6-j8})-\bar{V}_i(\frac{1}{4+j4})=\frac{5(0^{\circ})}{5}$$

From these two equations, the unknown voltages V, and V, must be determined.

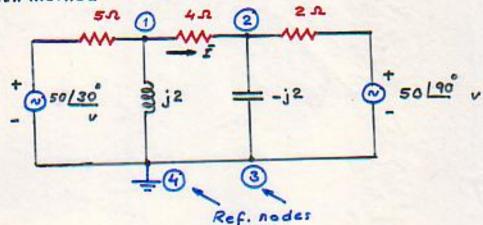


the owners flowing through the branch of 4.52 resistance using :

(a). Nodal Analysis

(b). Thevening theorem.

(c). Mesh method



Solution

(a). By Nodal Analysis Method

Node 1
$$\bar{V}_{1}\left(\frac{1}{5} + \frac{1}{4} + \frac{1}{j2}\right) - \bar{V}_{2}\left(\frac{1}{4}\right) = \frac{50(30^{\circ})}{5}$$

$$\Rightarrow \bar{V}_{1}\left(9 - j10\right) - 5\bar{V}_{2} = 200[30^{\circ}) - 0$$

$$Node 2$$

$$\bar{V}_{2}\left(\frac{1}{4} + \frac{1}{2} + \frac{1}{-j2}\right) - \bar{V}_{1}\left(\frac{1}{4}\right) = \frac{50[90^{\circ})}{2}$$

$$\Rightarrow \bar{V}_{2}\left(3 + j2\right) - \bar{V}_{1} = j100 - 2$$

solving Equ. 1 and 2; we find:

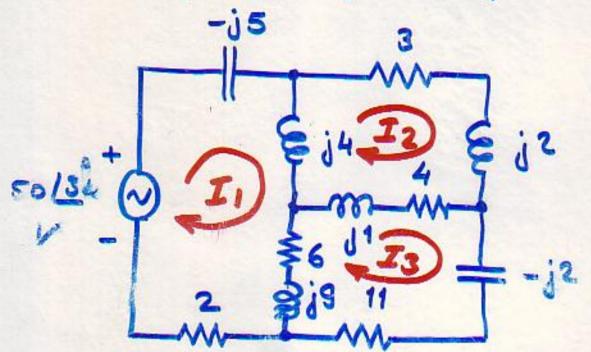
$$\bar{V}_{i} = j 27.26 \quad \text{valts}$$
and
$$\bar{V}_{2} = 19.58 + j 29.36$$
The current $\bar{I} = \frac{V_{i} - V_{2}}{4} = \frac{j 27.26 - 19.58 - j 29.36}{4}$

$$= \frac{19.69 \left[186.12^{\circ}\right]}{4} = 4.92 \left[186.12^{\circ}\right] A$$

Try to solve again using the methods of (b), and (c) and getting the same results.

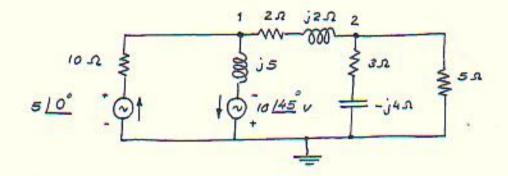
Homework (B)

For the cct. shown, find the current I, using the loop Method



TSS

- Write the nodal voltage equations for the circuit shown.



Solution

-: The nodal voltage equations are i

For Node 1

$$\left(\frac{1}{10} - \frac{1}{j5} + \frac{1}{2+j2}\right) \vec{V}_1 - \left(\frac{1}{2+j2}\right) \vec{V}_2 = \frac{5 \cdot 10^6}{10} - \frac{10 \cdot 145^6}{j5}$$

and

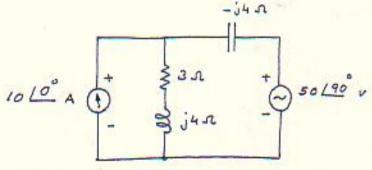
For Node 2

$$-\frac{1}{(2+j)^2} \Big) \bar{V_1} + \Big(\frac{1}{2+j^2} + \frac{1}{3-j^4} + \frac{1}{5}\Big) \bar{V_2} = 0$$

Solving the above equations to determine V, & V2.

Example

-: Apply the superposition theorem to determine the voltage drop across the (3+j4) & impedance , in the circuit shown.



Solution

+ The effect of (10 (0, A) source alone; Removing the solgo'v source and substitute it by a short circuit, then:

- 14 1 T39

+ using the current divider rule , then:

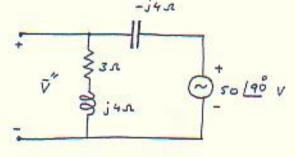
+ The effect of (50/90° v) voltage source; the circuit will be in this case as:

using the voltage divider rule , then:

$$\bar{V} = V_T \frac{\bar{Z}_2}{\bar{Z}_2 + \bar{Z}_1}$$

$$= (50 \frac{190}{})(\frac{(3+j4)}{(3+j4) + (-j4)})$$

$$= -66.7 + j50$$



$$\vec{v} = \vec{V}' + \vec{V}'' = (53.3 - j40) + (-66.7 + j50)$$

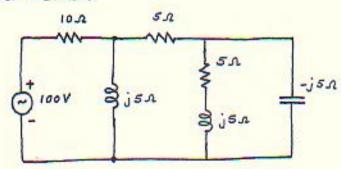
$$= -13.4 + j10 = 16.72 \frac{143.27}{143.27}$$

Example

: For the network shown, determine the voltage across the capacitor, using :

(a). Theventu's theorem.

(b). Mesh current method.





T59

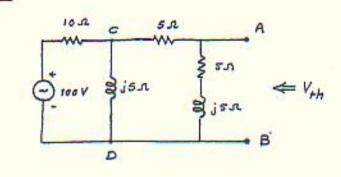
-: (a). Using Thevenin's theorem

vo Hage divider rule 3

$$\widehat{V}_{th} = \widehat{V}_{AB}$$

$$= \widehat{V}_{co} \frac{(s+js)}{(s+js)+(s)}$$

$$= \widehat{V}_{co} \frac{(s+js)}{(s+js)}$$

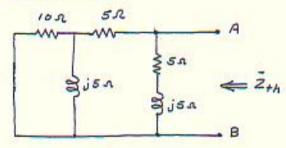


$$\bar{V}_{CD} = V_T \frac{(js)}{10+j5} = 100 \frac{(js)}{10+j5}$$

$$= 20+j40$$

Check

when looking through terminals A and B , the voltage source removed; the equivalent impedance Zth is determined from the circuit:

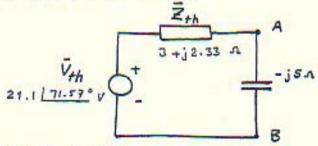


check

$$\bar{Z}_{th} = [(10/ljs) + s] / (5+js)$$

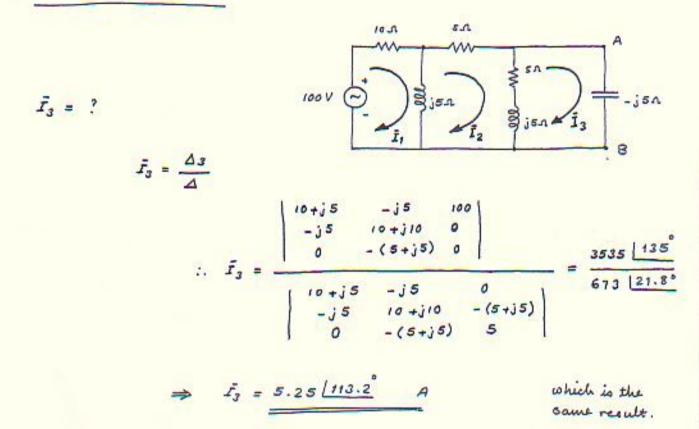
$$\therefore \bar{Z}_{th} = 3 + j \cdot 2.33 \quad \Lambda$$

@ Thevenin's equivalent circuit will be as shown:



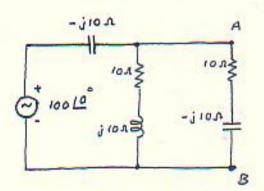
:. Total impedance
$$Z_T = (3+j2.53) + (-j5)$$
 $= 3-j2.67 = 4.02[-41.67^{\circ}] \Omega$
:. $I = \frac{V_{th}}{Z_T} = \frac{21.1 [71.57^{\circ}]}{4.02[-41.67^{\circ}]} = 5.25[113.24^{\circ}] A$

TSS



Example

____ : Use Norton's theorem to find the current in the load connected across terminals A and B of the circuit shown.

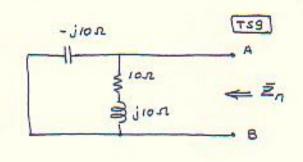


Solution

IN = ? > the current in the short circuit terminals A & B

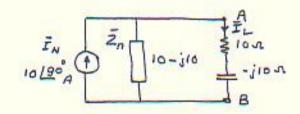
$$= \bar{z}_n = \bar{z}_{th} = ?$$

$$\bar{z}_n = (-j_{10}) / (10 + j_{10})
= \frac{(-j_{10})(10 + j_{10})}{(-j_{10}) + (10 + j_{10})}$$



+ The Norton's equivalent circuit is:

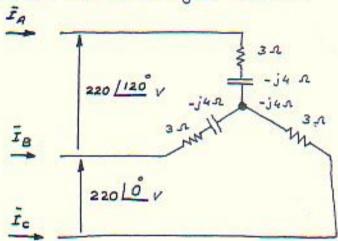
$$\bar{I_L} = \frac{\bar{I_N}}{2} = \frac{10 L90}{2}$$
$$= 5 L90 \quad A$$



Since IN is equally divided between the two equal impedances.

TS 9

----: For the circuit shown, determine the currents I_A , I_B and I_C , using the mesh current analysis method.



Solution

Using the mesh method , we have to determine the currents

Mesh Equations

(6- j8) I, - (3-j4) Iz = 220 L/20

I, and I, then:

and

 I_1 I_1 I_2 I_3 I_4 I_3 I_4 I_3 I_4 I_4 I_5 I_5

ama

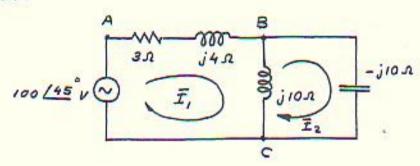
$$\bar{I}_{2} = \frac{\begin{vmatrix} (4-j8) & 220 & | & 120 & | \\ -(3-j4) & 220 & | & 00 & | \end{vmatrix}}{4} = \frac{1905 & | & -23.2 & |}{75 & | & -106.2} = 25.4 & | & 83 & | A$$

$$\vec{I}_{A} = \vec{I}_{1} = 25.4 \frac{143}{43} A ,$$

$$\vec{I}_{B} = \vec{I}_{2} - \vec{I}_{1} = 25.4 \frac{163}{5} - 25.4 \frac{143.1}{43.1} = 25.4 \frac{123.1}{5} A$$

$$\vec{I}_{C} = -I_{2} = -(25.4 \frac{183}{5}) = 25.4 \frac{1-97}{5} A$$

TS9



Solution

must be determined.

The 2 - mesh current equations are:

and

$$\bar{I}_{2} = \frac{\begin{vmatrix} 3+j4 & (00)[4s^{2}] \\ -j10 & 0 \end{vmatrix}}{\begin{vmatrix} 3+j4 & -j10 \\ -j10 & 0 \end{vmatrix}} = \frac{1000[13s]}{100} = 10[13s]$$

:.
$$\bar{V}_{AB} = \bar{I}_{1}(3+j4) = O(3+j4) = O$$

oud VBC = I2 (-310) = (10 [135°)(10 [-90°) = 100 [45°

For check:
$$V_T = V_{AB} + V_{BC}$$

$$= 0 + 100 [45] = 100 [45] = Applied Voltage$$